



H12SSW-IN  
H12SSW-NT

USER'S MANUAL

Revision 1.0a

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# Preface

## About This Manual

This manual is written for system integrators, IT technicians and knowledgeable end users. It provides information for the installation and use of the H12SSW-iN/NT motherboard.

## About This Motherboard

Built upon the functionality and capability of the AMD EPYC® 7002 series processor, the H12SSW-iN/NT motherboard provides a high performance, expandable solution while consuming little power. Please note that this motherboard is intended to be installed and serviced by professional technicians only. For processor/memory updates, please refer to our website at <http://www.supermicro.com/products/>.

## Conventions Used in the Manual

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:



**Warning!** Indicates important information given to prevent equipment/property damage or personal injury.



**Warning!** Indicates high voltage may be encountered when performing a procedure.

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# Chapter 1

## Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro motherboards are designed to provide you with the highest standards in quality and performance.

In addition to the motherboard, several important parts that are included with the system are listed below. If anything listed is damaged or missing, please contact your retailer.

### 1.1 Checklist

| Main Parts List                               |                                       |          |
|---|---------------------------------------|----------|
| Description                                   | Part Number                           | Quantity |
| Supermicro motherboard-H12SSW-iN or H12SSW-NT | MBD-H12SSW-IN-P or<br>MBD-H12SSW-NT-P | 1        |
| Slimline SAS x8 (LA) to SATA Cable            | CBL-SAST-0813                         | 2        |

### Important Links

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: <https://www.supermicro.com/support/manuals/>
- Product drivers and utilities: <https://www.supermicro.com/wftp/driver/>
- Product safety info: [https://www.supermicro.com/about/policies/safety\\_information.cfm](https://www.supermicro.com/about/policies/safety_information.cfm)
- If you have any questions, please contact our support team at: [support@supermicro.com](mailto:support@supermicro.com)

This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.



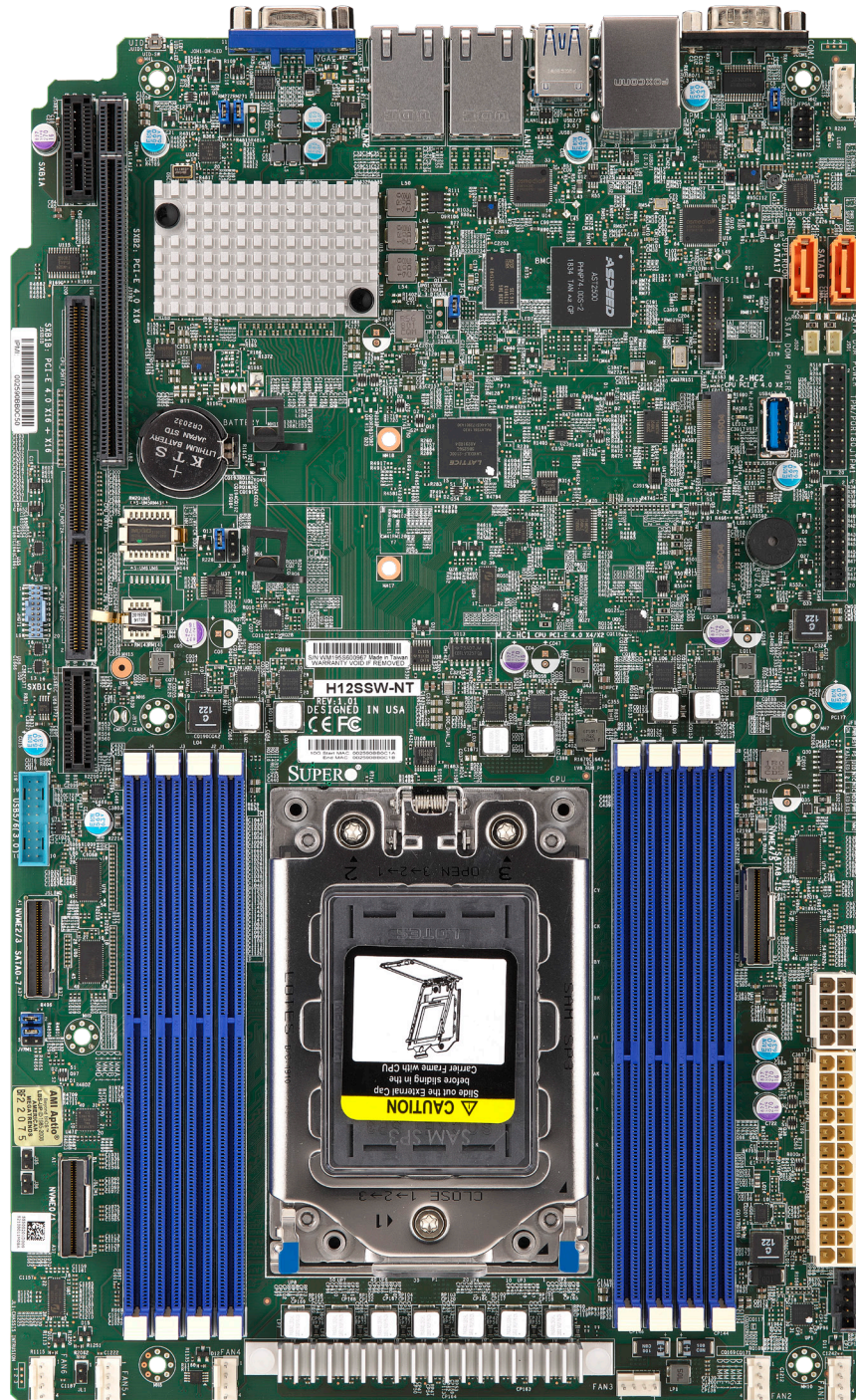


Figure 1-1. H12SSW-iN/NT Image

**Note:** All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

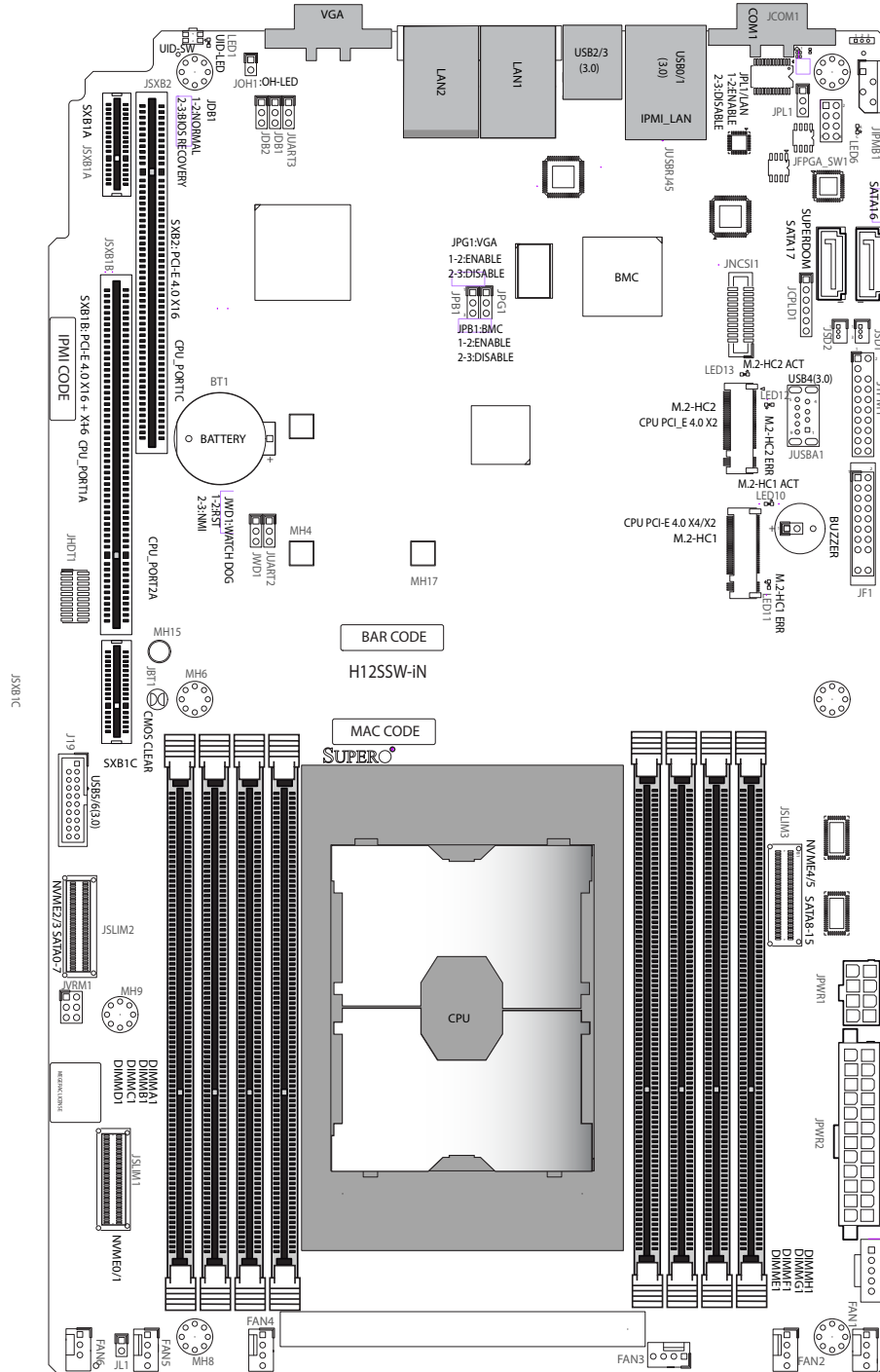
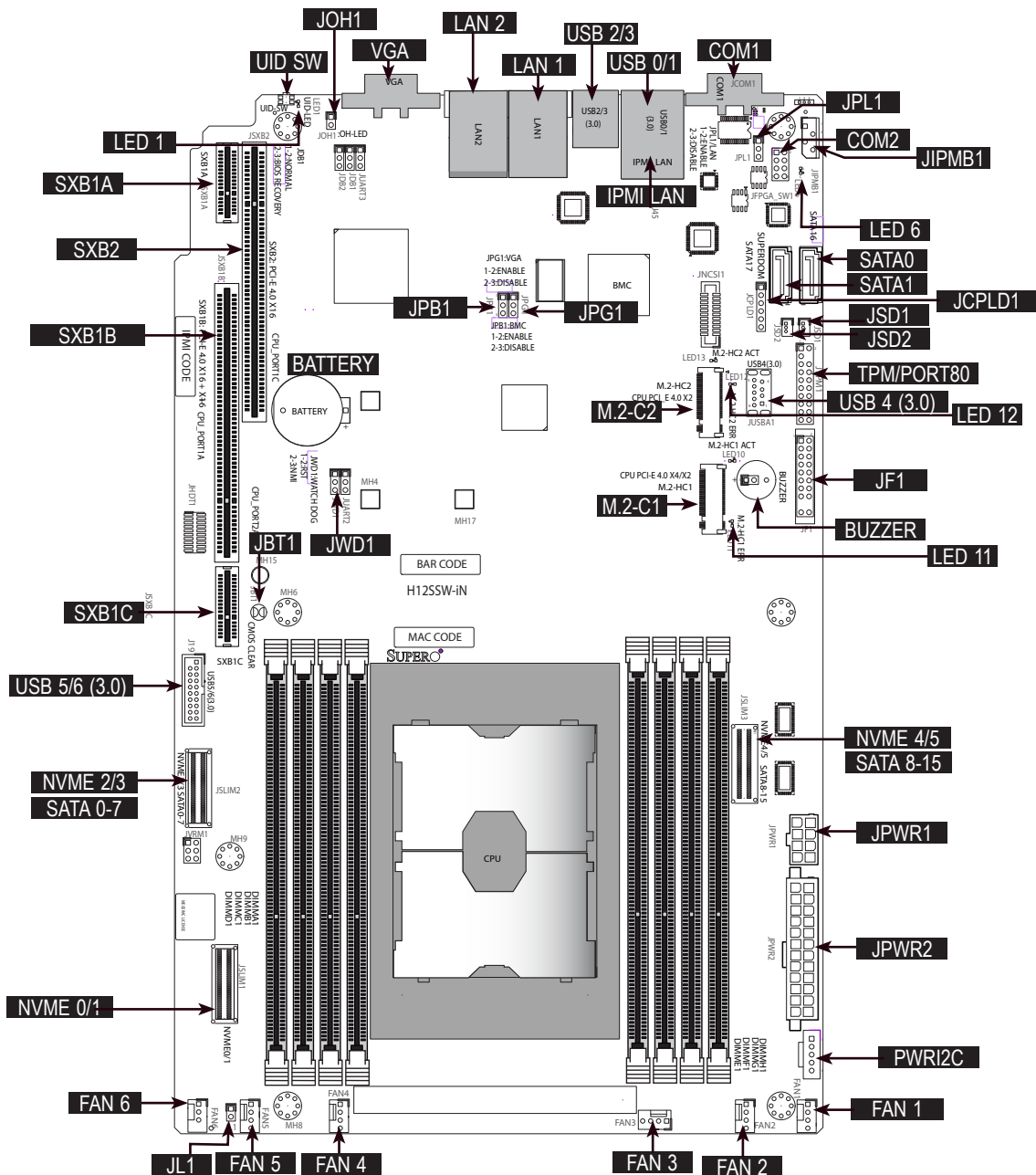


Figure 1-2. H12SSW-iN/NT Layout

## 1.1 Quick Reference



### Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- Jumpers/LED indicators not indicated are used for testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

## Quick Reference Table

| Jumper | Description                                       | Default Setting    |
|--------|---|--------------------|
| UID SW | Unit ID switch (push-button toggle switch ON/OFF) | Off                |
| JBT1   | Clear CMOS  | Open (Normal)      |
| JWD1   | Watch Dog Timer control                           | Pins 1-2 (Reset)   |
| JPL1   | LAN Enable/Disable (H12SSW-NT only)               | Pins 1-2 (Enabled) |
| JPG1   | Onboard Video Disable/Enable                      | Pins 1-2 (Enabled) |

| LED   | Description                | Status                       |
|-------|----------------------------|------------------------------|
| LED1  | UID LED                    | Blue: UID Activated          |
| LED6  | Power LED                  | On: Power On                 |
| LED 7 | BMC Heartbeat              | Blinking: BMC Normal/Working |
| LED12 | M.2-C2 Interface Detection | On: SATA Off: PCI-E          |
| LED11 | M.2-C1 Interface Detection | On: SATA Off: PCI-E          |

| Connector          | Description                                       |
|--------------------|---|
| Battery (BT1)      | Onboard CMOS battery                              |
| COM 1, COM 2       | Rear panel COM port #1, Header for COM port #2    |
| JCPLD1             | Complex-Programmable Logical Device (CPLD) header |
| FAN 1~6            | System cooling fan headers                        |
| JF1                | Front control panel                               |
| JSD1, JSD2         | SATA DOM power connector                          |
| JTPM1              | Trusted Platform Module (TPM)/Port 80 connector   |
| SATA0, SATA1       | Internal SATA Ports                               |
| JL1                | Chassis intrusion header                          |
| JOH1               | Chassis overheat header                           |
| USB 0/1 (3.0)      | Back panel USB 3.0 ports (USB 0/1)                |
| USB 2/3 (3.0)      | Back panel USB 3.0 ports (USB 2/3)                |
| USB 4 (3.0)        | USB 3.0 type A port (USB 4)                       |
| USB 5/6 (3.0)      | Internal USB 3.0 header (USB 5/6)                 |
| SXB1A, SXB2, SXB1C | Riser slots                                       |
| JPWR2              | 24-pin ATX power supply connector                 |
| JPWR1              | 12V 8-pin ATX CPU power connector                 |

**Note:** The table above is continued on the next page.

| Connector      | Description                                      |
|----------------|--|
| M.2-C1, M.2-C2 | M.2 Slots  |
| PWRI2C         | Power supply SMBus I2C header                    |
| JIPMB1         | 4-pin External BMC I2C Header (for an IPMI Card) |
| LAN1, LAN2     | Back panel LAN1, LAN2 connectors                 |
| VGA            | Back panel VGA port                              |
| NVMe0~5        | NVMe slots 0~5                                   |

**Note:** Jumpers, connectors, switches, and LED indicators that are not described in the preceding tables are for manufacturing testing purposes only, and are not covered in this manual.

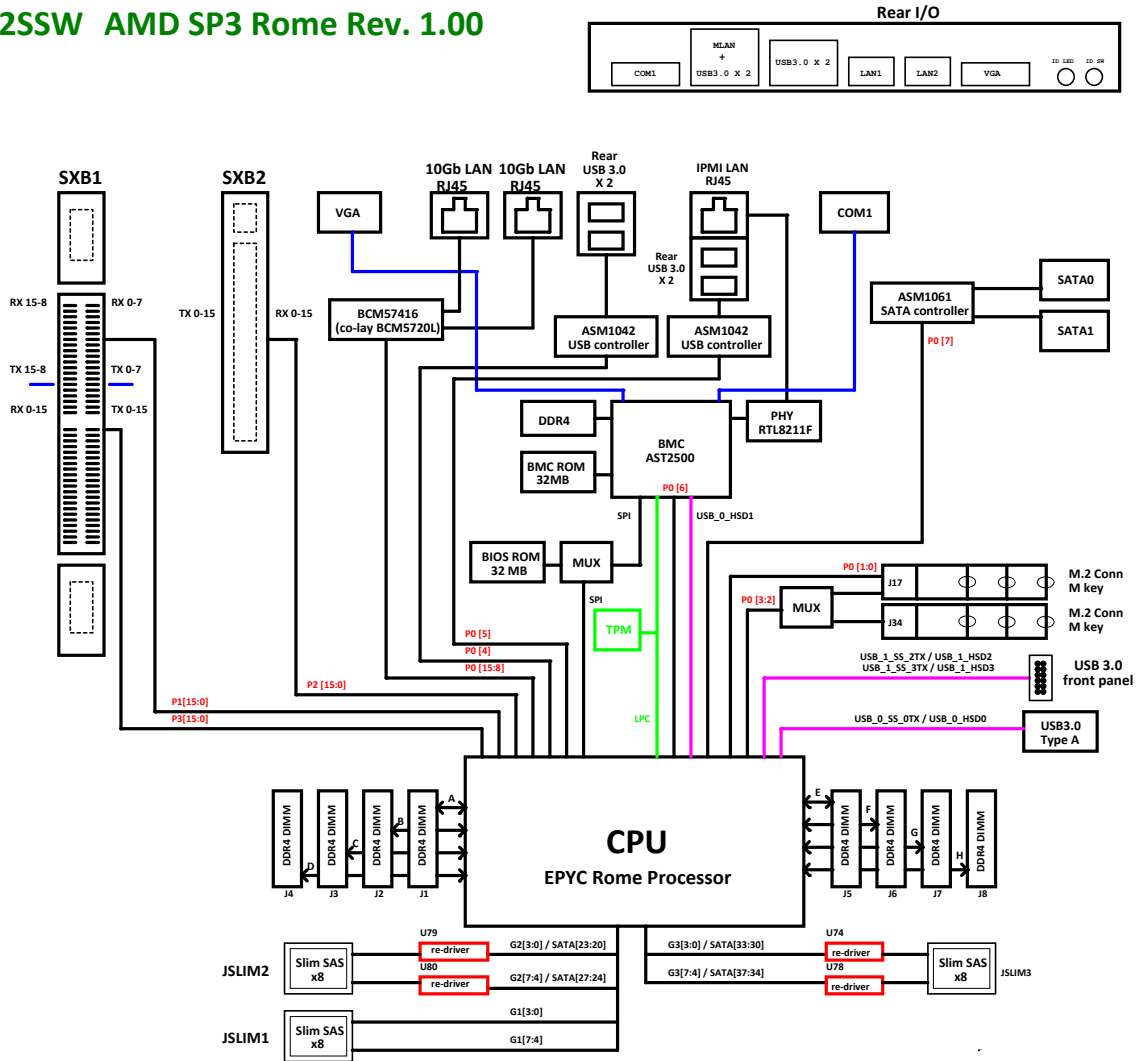
## Motherboard Features

| <b>Features</b>  |
|--|
| <b>CPU</b>   |
| <ul style="list-style-type: none"> <li>Single AMD EPYC® 7002 series processor, in one SP3 socket</li> </ul>  |
| <b>Memory</b>  |
| <ul style="list-style-type: none"> <li>Up to two TB of ECC DDR4 3200 MHz speed, RDIMM/LRDIMM/3DS/NVDIMM memory in eight slots</li> </ul>   |
| <b>DIMM Size</b>   |
| <ul style="list-style-type: none"> <li>Up to 128GB size at 1.2V</li> </ul>   |
| <b>Chipset</b>   |
| <ul style="list-style-type: none"> <li>System on Chip</li> </ul>   |
| <b>Expansion Slots</b>   |
| <ul style="list-style-type: none"> <li>1x PCI-E 4.0 (x32) Left Riser Slot</li> <li>1x PCI-E 4.0 (x16) Right Riser Slot</li> <li>M.2 Interface: 2x PCI-E 3.0 x2/SATA</li> <li>M.2 Form Factor: 2280, 22110</li> <li>M.2 Key: M-Key</li> </ul> |
| <b>Network</b>   |
| <ul style="list-style-type: none"> <li>ATEN IPMI from ASPEED AST 2500 BMC for gigabit RJ45 port</li> <li>Two RJ45 1GbE LAN ports, H12SSW-iN(1GbE Broadcom 5720L); H12SSW-NT(10GbE Broadcom 57416)</li> </ul>                                 |
| <b>Graphics</b>  |
| <ul style="list-style-type: none"> <li>ASPEED AST2500 BMC chip with one VGA port</li> </ul>  |
| <b>I/O Devices</b>   |
| <ul style="list-style-type: none"> <li>One COM RS-232 type connector</li> <li>One COM header</li> <li>Six USB 3.0 headers</li> <li>One USB 3.0 Type A connector</li> </ul>   |
| <b>Peripheral Devices</b>  |
| <ul style="list-style-type: none"> <li>Two native NVMe Ports (SlimSAS x8); Four internal NVMe or sixteen SATA dual use ports (SlimSAS x8)</li> <li>Two SATA DOM ports</li> </ul>   |
| <b>BIOS</b>  |
| <ul style="list-style-type: none"> <li>128Mb SPI AMI BIOS®</li> <li>ACPI 6.2, SMBIOS 3.1.1, Plug-and-Play (PnP), BIOS rescue hot-key, RTC (Real Time Clock) wake up, Riser Card Auto-Detection Support</li> </ul>                            |

**Note:** The table above is continued on the next page.

| <b>Features</b>   |
|---|
| <b>Power Management</b>   |
| <ul style="list-style-type: none"><li>• ACPI power management (S5)</li><li>• Power button override mechanism</li><li>• Power-on mode for AC power recovery</li></ul>  |
| <b>System Health Monitoring</b>   |
| <ul style="list-style-type: none"><li>• Onboard voltage monitoring for +1.8V, 3.3V, +5V, +12V, +3.3V Standby, +5V Standby, VBAT, HT, Memory</li><li>• Onboard monitoring for CPU, system, and memory temperature</li><li>• CPU switching phase voltage regulator</li><li>• CPU Thermal Trip support</li></ul> |
| <b>Fan Control</b>  |
| <ul style="list-style-type: none"><li>• Single cooling zone</li><li>• Low-noise fan speed control</li><li>• Pulse Width Modulation (PWM) fan control</li></ul>  |
| <b>System Management</b>  |
| <ul style="list-style-type: none"><li>• Trusted Platform Module (TPM) support</li><li>• System resource alert via SuperDoctor® 5</li><li>• SuperDoctor® 5, Watch Dog</li><li>• Non-Maskable Interrupt (NMI)</li><li>• SUM-InBand, SUM-OOB, IPMICFG, IPMIView, SMCIPMITOOL</li></ul>                           |
| <b>LED Indicators</b>   |
| <ul style="list-style-type: none"><li>• Power State Indicator</li><li>• CPU/Overheating</li><li>• Fan Failure</li><li>• LAN activity</li><li>• UID / Remote UID</li></ul>   |
| <b>Dimensions</b>   |
| <ul style="list-style-type: none"><li>• 8" (L) x 13" (W), 20.32cm (L) x 33.02cm (W)</li></ul>   |

**H12SSW AMD SP3 Rome Rev. 1.00**



**Figure 1-3. System Block Diagram**

**Note:** This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.



## 1.2 Processor and Chipset Overview

The H12SSW-iN/NT motherboard offers maximum I/O expandability, energy efficiency, and data reliability in a 7-nm process architecture. It is optimized for high-performance computing, NVMe storage solutions, and ideal for High Density Data Center applications.

The H12SSW-iN/NT supports the new 7 nm microarchitecture process technology, which significantly increases system performance for a multitude of server applications.

The AMD EPYC® 7002 series processor supports the following features:

- ACPI Power Management Logic Support Rev. 6.2
- Adaptive Thermal Management/Monitoring
- PCI-E 4.0 w/transfer rates of up to 16 Gb/s
- SATA 3.0 w/transfer rates of up to 6 Gb/s
- System Management Bus (SMBus) Specification Version 2.0

## 1.3 Special Features

This section describes the health monitoring features of the H12SSW-iN/NT. The motherboard has an onboard System Hardware Monitor chip that supports system health monitoring.

### Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is Last State.

## 1.4 System Health Monitoring

This section describes the health monitoring features of the H12SSW-iN/NT motherboard. The motherboard has an onboard Baseboard Management Controller (BMC) chip that supports system health monitoring. Once a voltage becomes unstable, a warning is given or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

## Onboard Voltage Monitors

The onboard voltage monitor will continuously scan crucial voltage levels. Once a voltage becomes unstable, it will give a warning or send an error message to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor. Real time readings of these voltage levels are all displayed in BMC.

## Fan Status Monitor with Firmware Control

Users can check the RPM status of the cooling fans through the IPMI Web interface. The chassis fans are controlled by Thermal Management.

## Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once the thermal sensor detects that the CPU temperature is too high, it will automatically turn on the thermal fans to prevent the CPU from overheating. The onboard chassis thermal circuitry can monitor the overall system temperature and alert the user when the chassis temperature is too high.

**Note:** To avoid possible system overheating, please be sure to provide adequate airflow to your system.

## System Resource Alert

This feature is available when used with SuperDoctor 5<sup>®</sup>. SuperDoctor 5 is used to notify the user of certain system events. For example, you can configure SuperDoctor 5 to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond a predefined range.

## 1.5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows 2016 and 2019 operating systems.

## 1.6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates. In areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

## 1.7 Super I/O

The ASpeed AST2500 Super I/O provides one high-speed, 16550 compatible Universal Asynchronous Receiver/Transmitter (UART), which support serial infrared communications. This UART includes a send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. This UART provides legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through the System Management Interrupt (SMI) or System Control Interrupt (SCI). It also features auto power management to reduce power consumption.

## Chapter 2

# Installation

### 2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your motherboard, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

#### Precautions

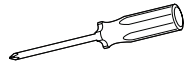
- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of CMOS onboard battery as specified by the manufacturer. Do not install the CMOS battery upside down, which may result in a possible explosion.

#### Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

## 2.2 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly.



**Phillips  
Screwdriver (1)**



**Phillips Screws**



**Standoffs  
(Only if needed)**

### Tools Needed

### Location of Mounting Holes

#### Notes:

1. To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation.
2. Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

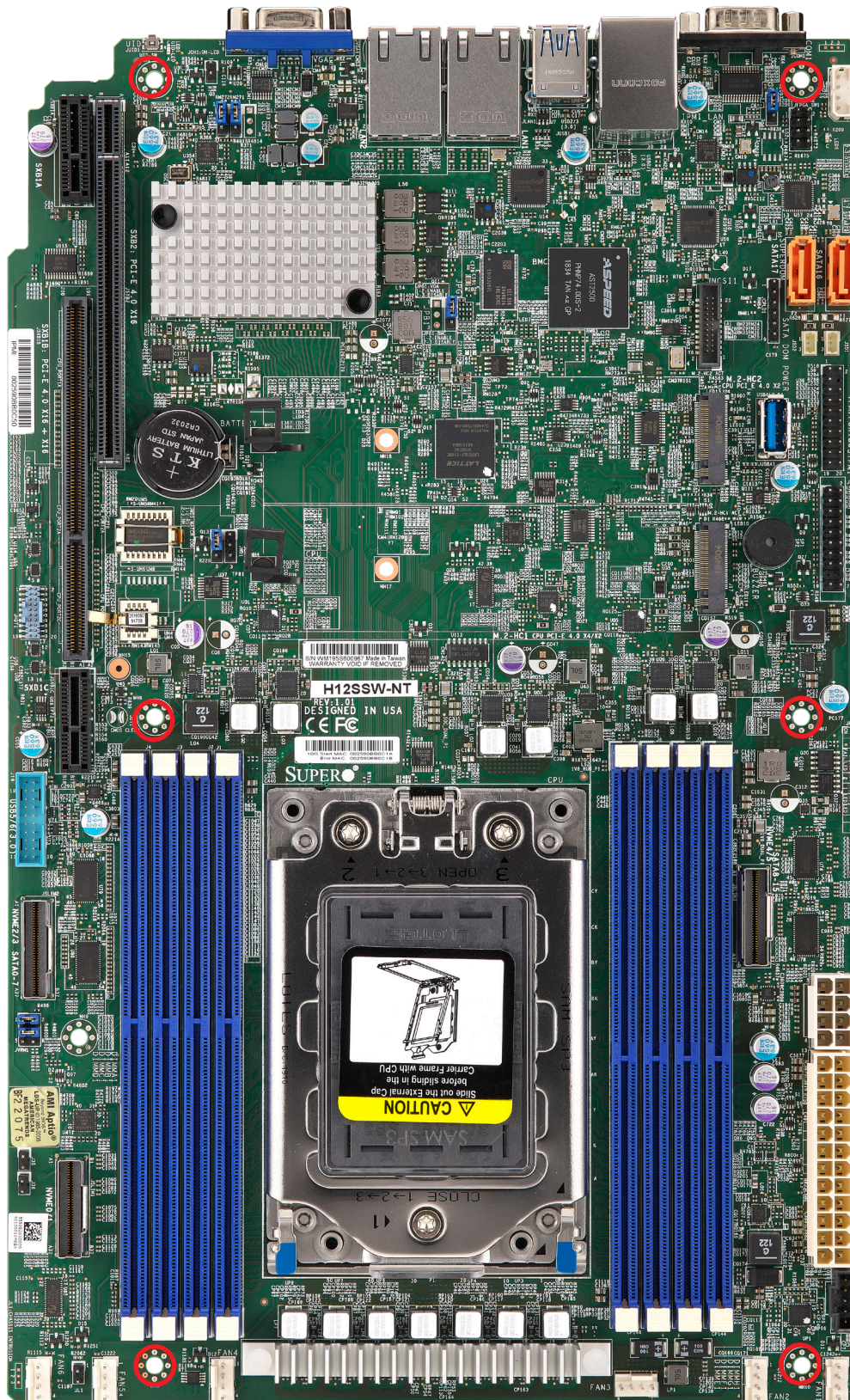
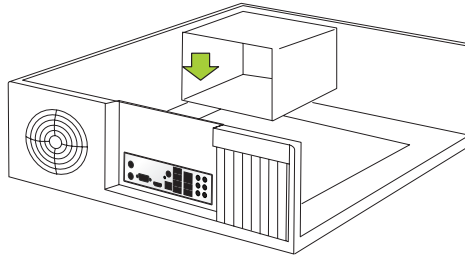


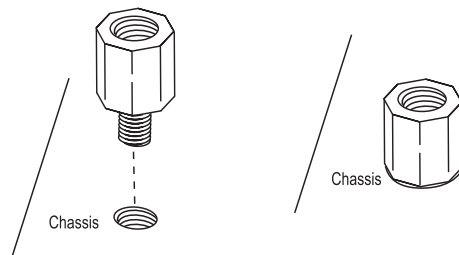
Figure 2-1. Motherboard Mounting Holes

## Installing the Motherboard

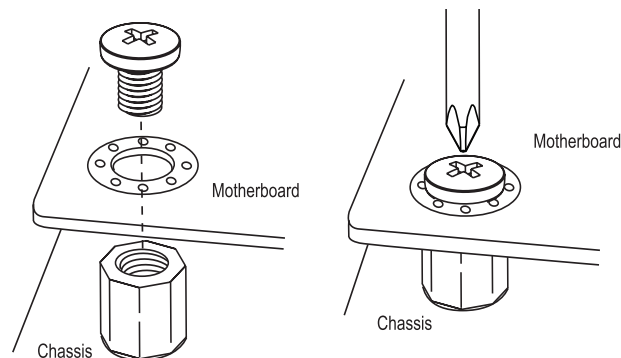
1. Install the I/O shield into the back of the chassis.



2. Locate the mounting holes on the motherboard. See the previous page for the locations.



3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard with the mounting holes on the chassis.



4. Install standoffs in the chassis as needed.
5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.
6. Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

**Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

## 2.3 Processor and Heatsink Installation

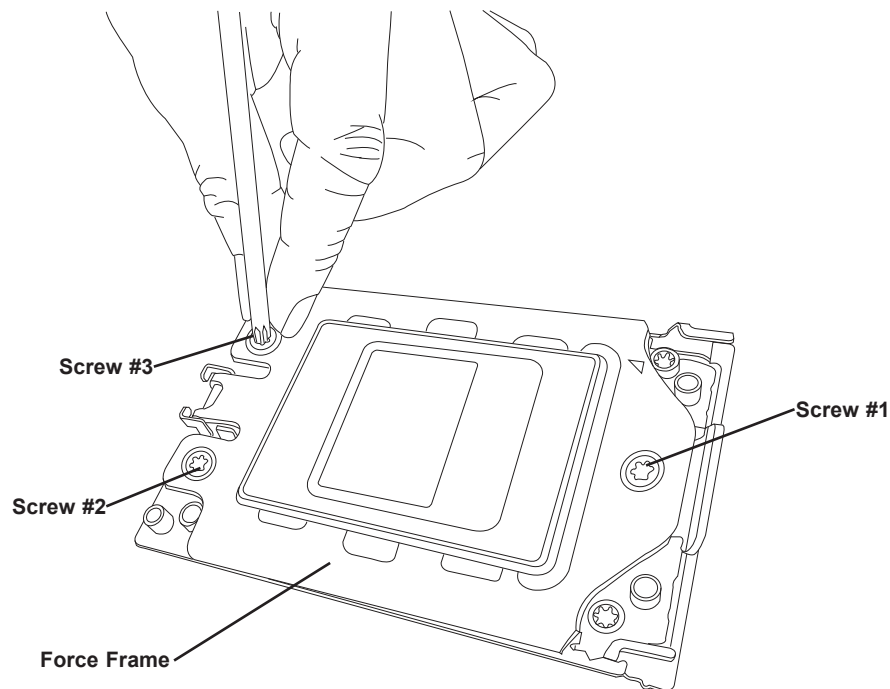
**Warning:** When handling the processor package, avoid placing direct pressure on the label area of the fan.

**Important:**

- For the Processor/Heatsink installation you need to use a T20 screwdriver when opening/closing the CPU socket.
- Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.
- If you buy a CPU separately, make sure that you use an AMD-certified heatsink only. Make sure to install the motherboard into the chassis before you install the CPU heatsink.
- When receiving a motherboard without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.
- Refer to the Supermicro website for updates on CPU support.

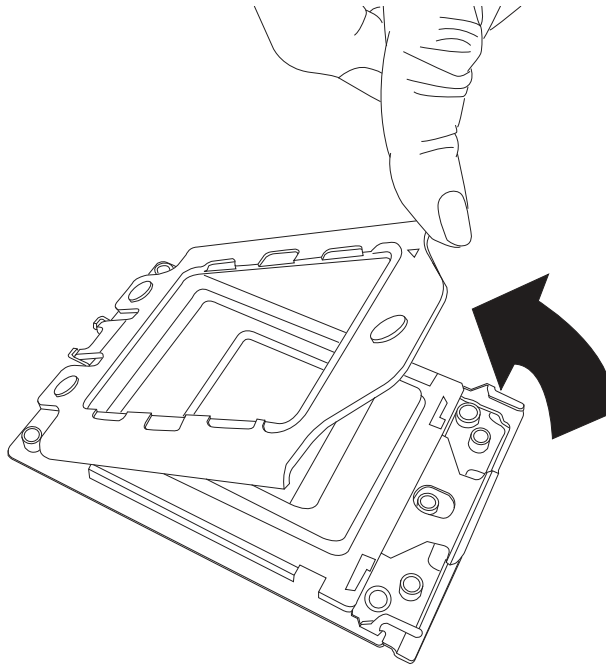
### Installing the Processor and Heatsink

1. Unscrew the screws holding down Force Frame in the sequence of 3-2-1. The screws are numbered on the force frame next to each screw hole.



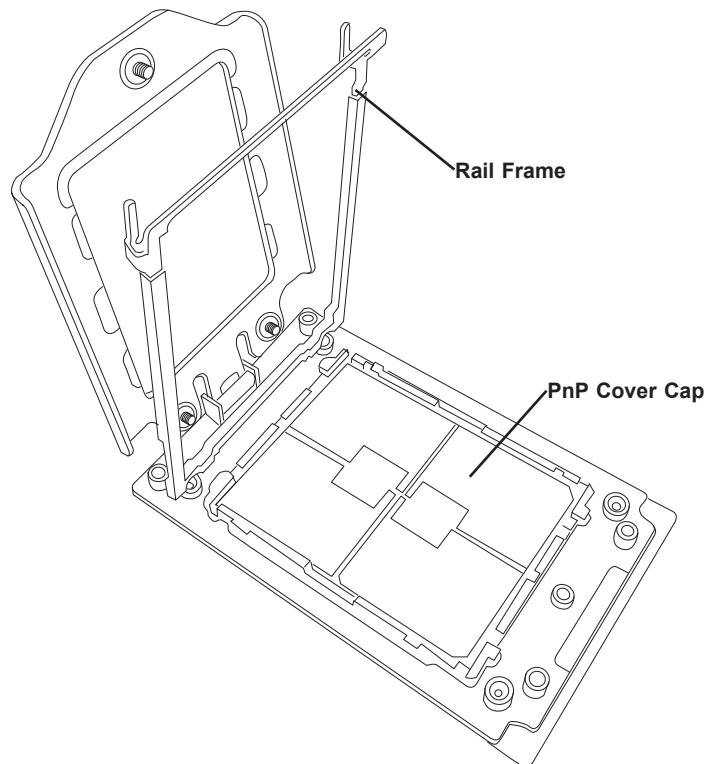


2. The spring-loaded force frame will raise up after the last screw securing it (#1) is removed. Gently allow it to lift up to its stopping position.

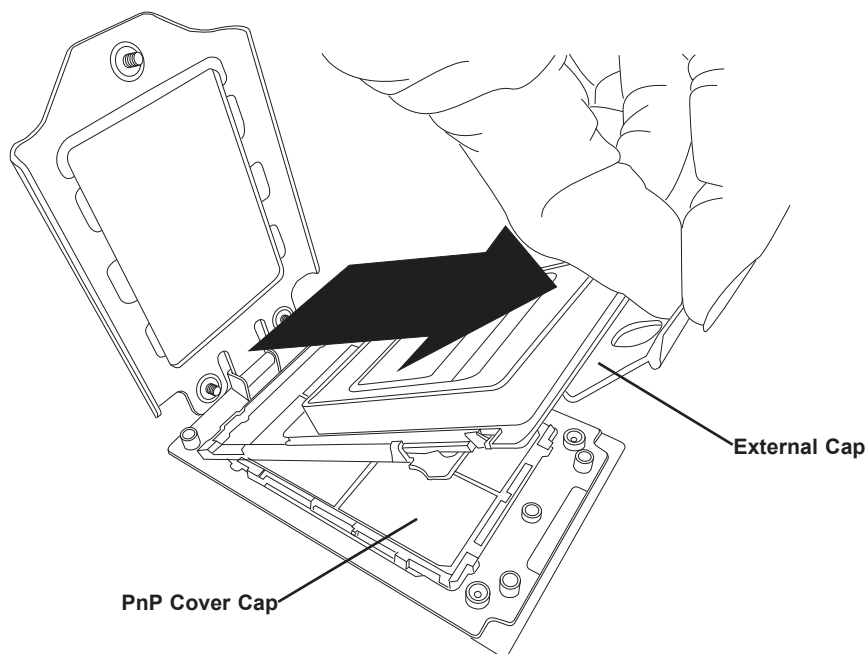


3. Lift the rail frame up by gripping the lift tabs near the front end of the rail frame. While keeping a secure grip of the rail frame, lift it to a position so you can do the next step of removing the external cap.

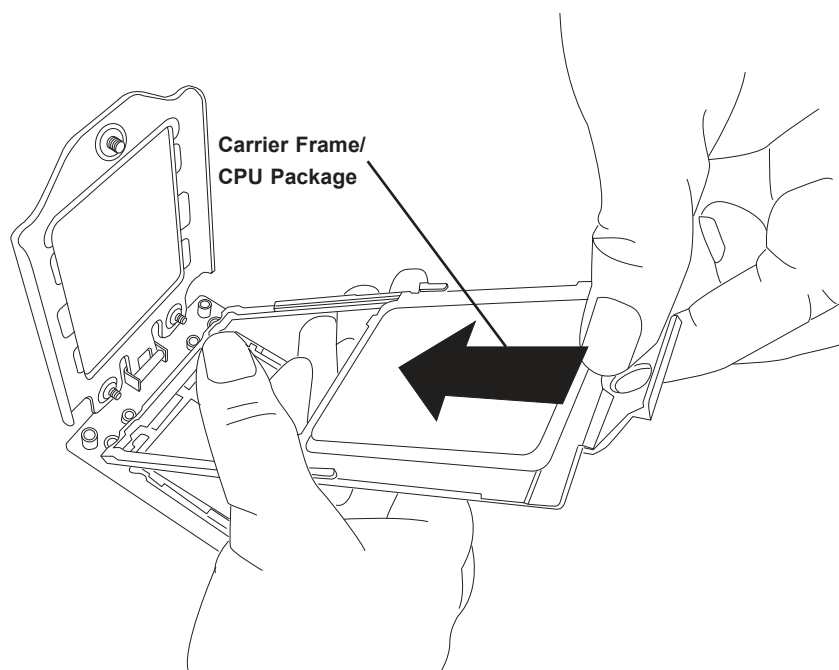
**Note:** The rail frame is spring loaded, so keep a secure grip on it as you lift it so it does not snap up.



4. Remove the external cap from the Rail Frame by pulling it upwards through the rail guides on the rail frame.



5. The CPU package is shipped from the factory with the blue carrier frame pre-assembled. Grip the handle of the carrier frame/CPU package assembly from its shipping tray, and while gripping the handle, align the flanges of the carrier frame onto the rails of the rail frame so its pins will be at the bottom when the rail frame is lowered later.
6. Slide the carrier frame/CPU package downwards to the bottom of the rail frame. Ensure the flanges are secure on the rails as you lower it downwards.

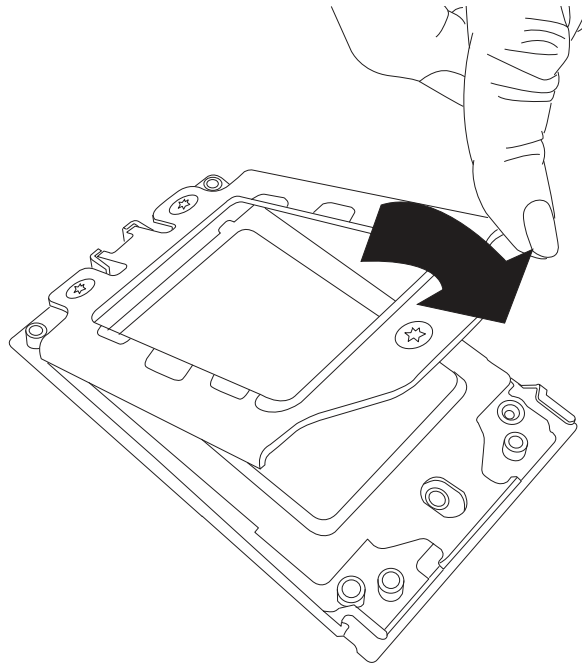


**Note:** You can only install the CPU inside the socket in one direction with the handle at the top. Make sure that it is properly inserted into the CPU socket before closing the rail frame plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the rail frame plate again, and double-check that the CPU is aligned properly.

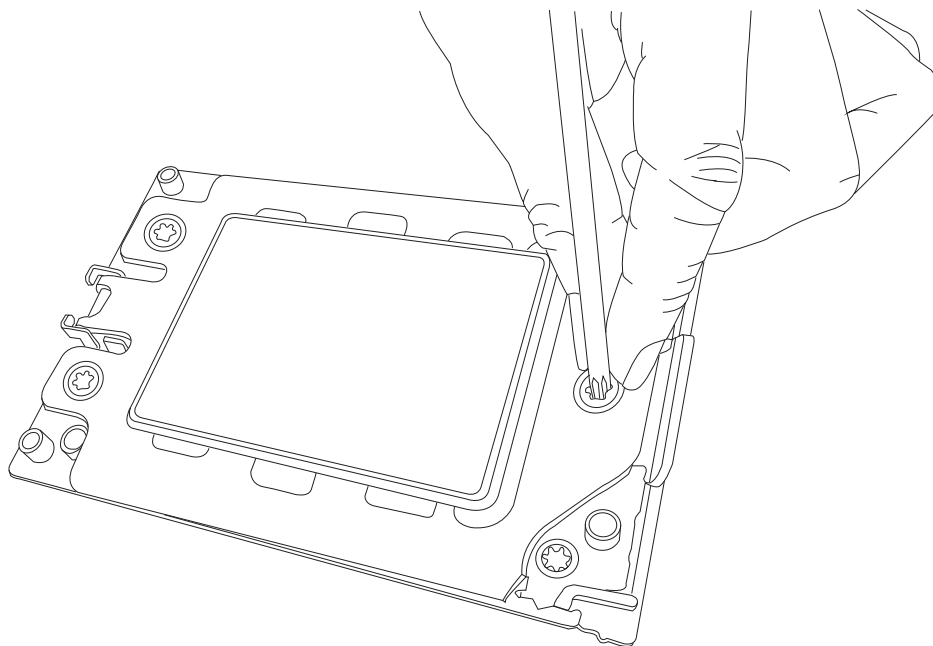
7. Lift up the rail frame till it securely rests in upright position. Then remove the PnP cover cap from the CPU socket below. Grip the two lift tabs marked "Remove" at the middle of the cap and pull vertically upwards to remove the PnP cover cap.

**Warning!** The exposed socket contacts are extremely vulnerable and can be damaged easily. Do not touch or drop objects onto the contacts and be careful removing the PnP cover cap and when placing the rail frame over the socket.

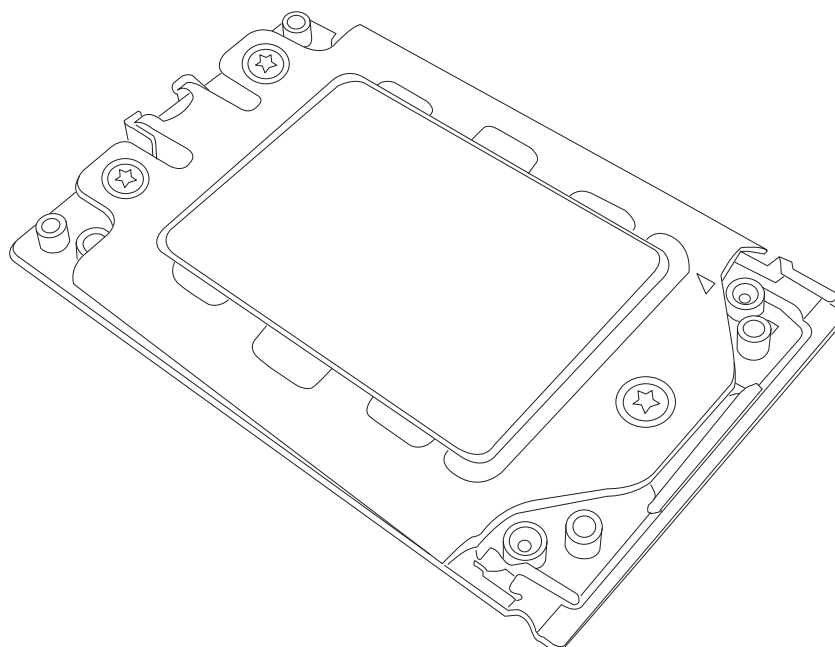
8. Gently lower the rail frame down onto the socket until the latches on the rail frame engage with the socket housing and it rests in place. **DO NOT** force it into place!



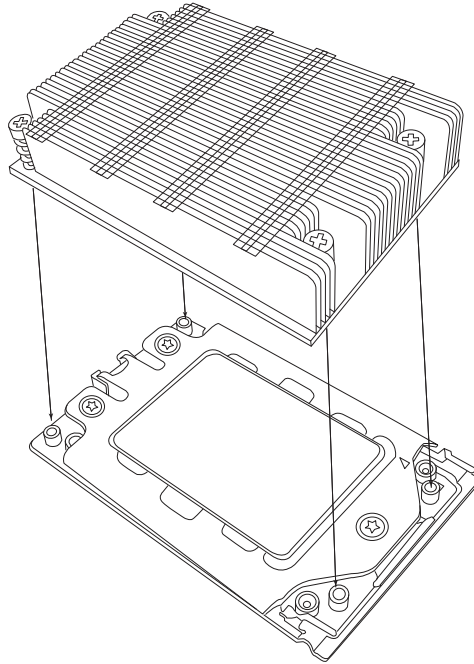
9. Gently lower the force frame down onto the rail frame and hold it in place until it is seated in the Socket housing. Note that the force frame is spring loaded and has to be held in place before it is secured. **Important: Use a torque screwdriver, set it at 16.1 kgf-cm (14.0 lbf-in) with a Torx T20 screw head bit, to prevent damage to the CPU.**



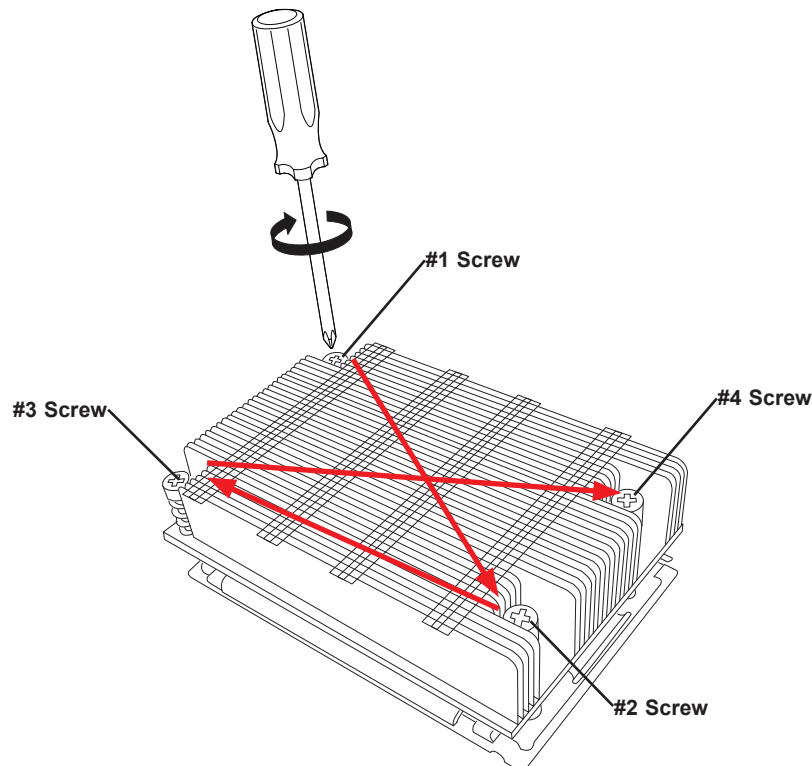
10. Place and re-screw the screws in the reverse order to the way you removed them (holes 1-2-3 in order). When finished, the force frame will be secure over both the rail frame and CPU package.



11. After the force frame is secured and the CPU package is in place, now you must install the heatsink to the frame. Lower the heatsink down till it rests securely over the four screw holes on CPU package on the socket frame.



12. Using a diagonal pattern, tighten the four screws down on the heatsink in a clockwise fashion till it is secure. The heatsink will now be secured and you have finished installing the processor and heatsink onto the motherboard. Repeat this procedure for any remaining CPU sockets on the motherboard.



### **Uninstalling the Processor and Heatsink**

1. Remove the heatsink attached to the top of the CPU package by reversing the installation procedure.
2. Clean the Thermal grease left by the heatsink on the CPU package lid to limit the risk of it contaminating the CPU package land pads or contacts in the socket housing.
3. Reverse the procedure for installing the force frame onto the socket, unscrewing the plate in the 3-2-1 screw order and lift the force frame to the vertical position.
4. Lift the rail frame using the lift tabs near the front end of the rail frame. Note that the rail frame is spring loaded, so be careful lifting it up into a vertical position.
5. Grip the handle of the carrier frame and pull upwards to extract it from the rail frame. Return the carrier frame/CPU package to its original shipping container.
6. Grip the handle on the external cap and return it to the rail frame sliding it downwards till it rests in the frame.
7. Gripping the rail frame, rotate it downwards till it rests above and locks over the socket housing in its horizontal position.
8. Push and rotate down the force frame till it is over the external cap and rail frame into a horizontal position.
9. While holding down the force frame, secure it back to the socket frame by securing screw 1 in place. Note that without a CPU package in place, it is not necessary to tighten down screws 2 and 3 at this time.

## 2.4 Memory Support and Installation

**Note:** Check the Supermicro website for recommended memory modules.

**Important:** Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

### *Memory Support*

The H12SSW-iN/NT supports up to two TB of ECC DDR4 3200MHz speed, RDIMM/LRDIMM/3DS/NVDIMM memory in eight slots. Refer to the table below for additional memory information.

| Populating RDIMM/RDIMM 3DS/LRDIMM/LRDIMM 3DS DDR4 Memory Modules |                   |                            |            |            |                         |
|--|-------------------|----------------------------|------------|------------|-------------------------|
| Type   | DIMM Population   | Maximum DIMM Capacity (GB) |            |            | Maximum Frequency (MHz) |
|  |                   | 1 Channel                  | 4 Channels | 8 Channels |                         |
| RDIMM  | 1R                | 32GB                       | 128GB      | 256GB      | 3200                    |
|  | 2R                | 64GB                       | 256GB      | 512GB      | 3200                    |
| LRDIMM   | 4R                | 128GB                      | 512GB      | 1TB        | 3200                    |
|  | 2S4R              | 256GB                      | 1TB        | 2TB        | 3200                    |
| LRDIMM 3DS   | 2S2R<br>(4 Ranks) | 128GB                      | 512GB      | 1TB        | 3200                    |
|  | 2S4R<br>(8 Ranks) | 256GB                      | 1TB        | 2TB        | 3200                    |

## DIMM Module Population

When populating the motherboard with DIMM modules, please keep in mind the following:

- Always use DDR4 DIMM modules of the same type, size and speed.
- All eight memory channels per CPU socket should be populated with each channel having equal capacity. This enables the memory subsystem to operate in eight-way interleaving mode, which should provide the best performance in most cases.
- In most configurations, populating fewer than eight channels is supported, but not recommended.

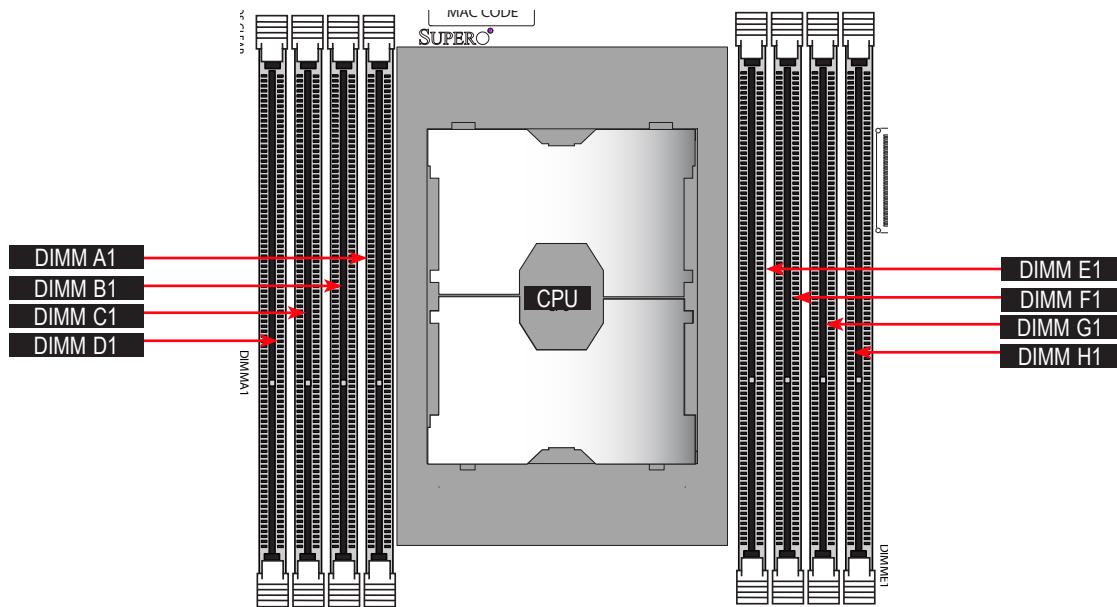


Figure 2-2. DIMM Numbering

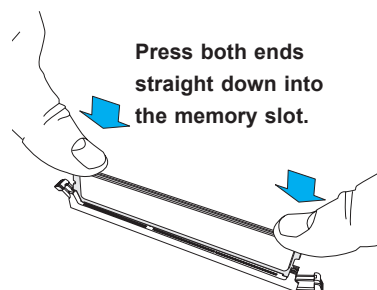
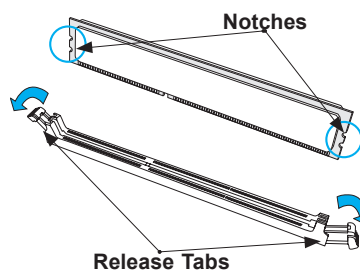
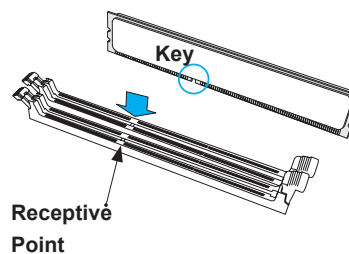
| The Processor and its Memory Module Distribution |                                      |    |    |    |    |    |    |    |
|--|--------------------------------------|----|----|----|----|----|----|----|
| CPU#   | Channel                              |    |    |    |    |    |    |    |
|  | D1                                   | C1 | B1 | A1 | E1 | F1 | G1 | H1 |
|  | 1 DIMM (Supported, Not Recommended)  |    |    |    |    |    |    |    |
| CPU1   |                                      | ✓  |    |    |    |    |    |    |
|  | 2 DIMMs (Supported, Not Recommended) |    |    |    |    |    |    |    |
| CPU1   | ✓                                    | ✓  |    |    |    |    |    |    |
|  | 4 DIMMs                              |    |    |    |    |    |    |    |
| CPU1   | ✓                                    | ✓  |    |    |    |    | ✓  | ✓  |
|  | 6 DIMMs                              |    |    |    |    |    |    |    |
| CPU1   | Unbalanced, not recommended          |    |    |    |    |    |    |    |
|  | 8 DIMMs                              |    |    |    |    |    |    |    |
| CPU1   | ✓                                    | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  |

\*NOTE: Most configurations populating fewer than eight channels are supported, but not recommended.



## DIMM Installation

1. Insert the desired number of DIMMs into the memory slots, there is no specific sequence or order required.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.
3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Press both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the DIMM module into the slot.



## DIMM Removal

Press both release tabs on the ends of the DIMM module to unlock it. Once the DIMM module is loosened, remove it from the memory slot.

## 2.5 Rear I/O Ports

See Figure below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

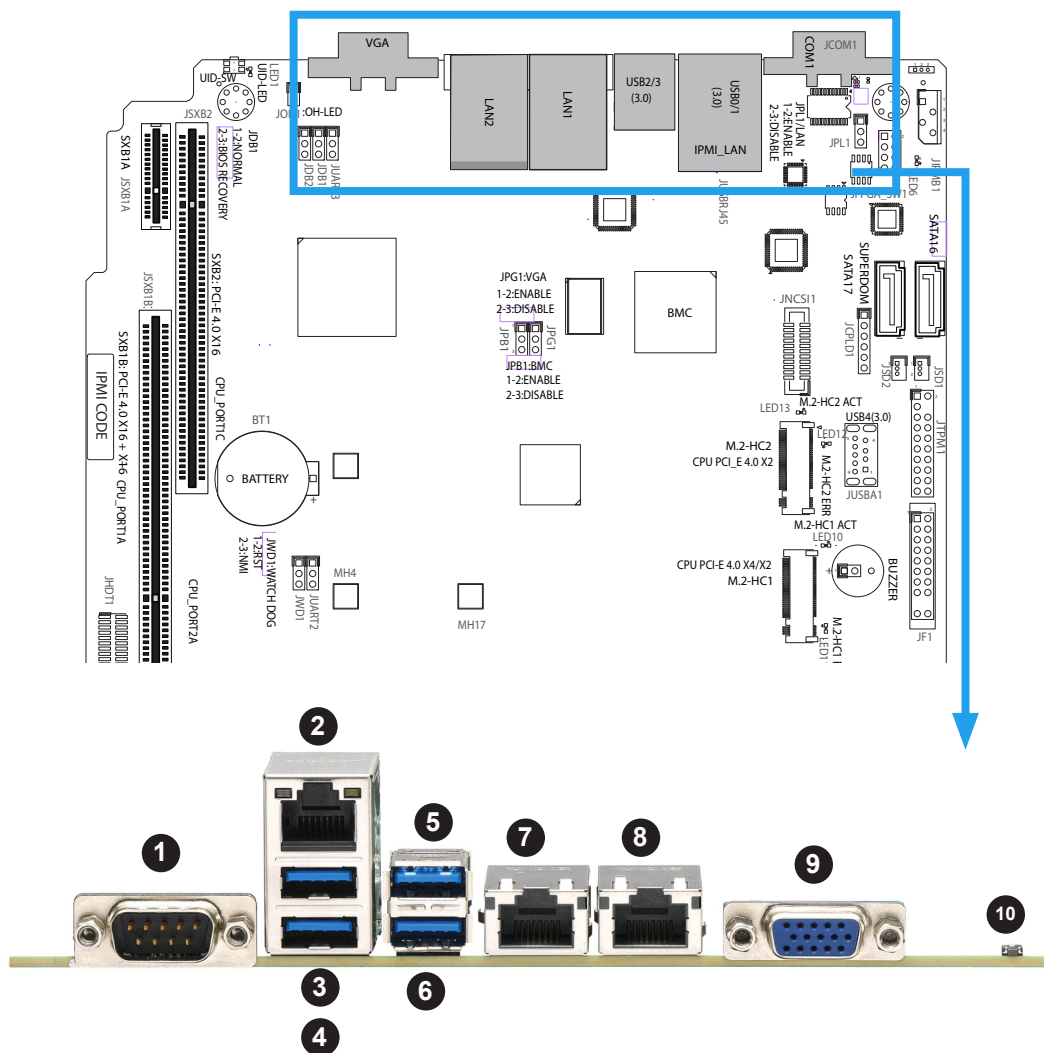


Figure 2-3. I/O Port Locations and Definitions

| Rear I/O Ports |               |   |             |    |                      |
|----------------|---------------|---|-------------|----|----------------------|
| #              | Description   | # | Description | #  | Description          |
| 1              | COM Port      | 5 | USB 2 (3.1) | 9  | VGA Port             |
| 2              | IPMI LAN Port | 6 | USB 3 (3.1) | 10 | UID Switch & UID LED |
| 3              | USB 0 (3,1)   | 7 | LAN Port #1 |    |                      |
| 4              | USB 1 (3.1)   | 8 | LAN Port #2 |    |                      |

**1. COM Port**

There is one serial communications port (COM1) on the rear I/O panel.

**2. IPMI LAN Port**

One IPMI LAN port is located on the I/O back panel. This port accepts an RJ45 type cable.

**3~6. Universal Serial Bus (USB) Ports**

There are four USB 3.1 ports (USB 0/1, 2/3) on the I/O back panel. These support the type A connector.

**7~8. Gigabit LAN Ports**

There are two gigabit LAN ports located on the I/O back panel (LAN1 & LAN2). These ports accept an RJ45 type cable.

**9. VGA Port**

There is one VGA port on the rear I/O panel.

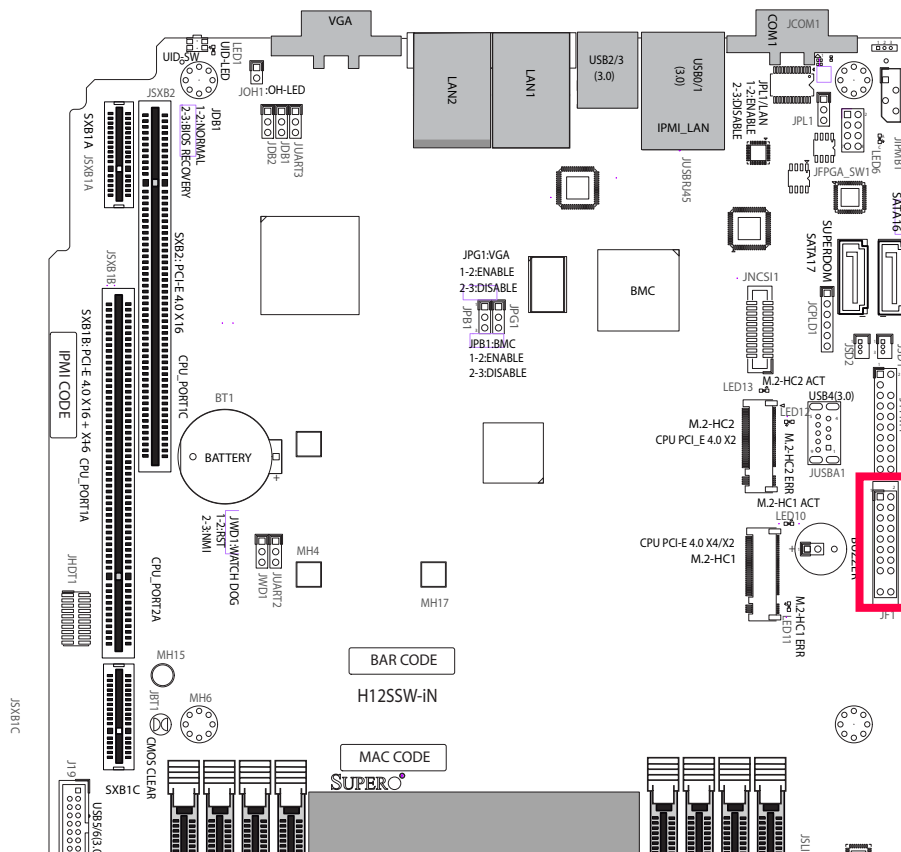
**10. UID Switch and UID LED Indicator**

A Unit Identifier (UID SW) switch and UID LED (LED1) are located on the I/O backpanel. The rear UID LED (LED1) is located next to the UID switch. When you press the UID switch, both rear and front UID LED indicators will turn on. Press the UID switch again to turn off the LED indicators. The UID Indicator provides easy identification of a system that may be in need of service. See page 36 for the front panel UID LED header location on JF1.

**Note:** UID can also be triggered via IPMI on the serverboard. For more information on IPMI, please refer to the IPMI User's Guide posted on our website @ <http://www.supermicro.com>

## 2.6 Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the location of JF1..



|                | 20 | 19 |                      |
|----------------|----|----|----------------------|
| NMI            | ○  | ○  | X                    |
| X              | ○  | ○  | X                    |
| PWR LED        | ○  | ○  | 3.3V                 |
| HDD LED        | ○  | ○  | 3.3V Stby            |
| NIC1 Link LED  | ○  | ○  | 3.3V Stby            |
| NIC2 Link LED  | ○  | ○  | 3.3V Stby            |
| UID LED        | ○  | ○  | 3.3V Stby            |
| Power Fail LED | ○  | ○  | 3.3 V                |
| Ground         | ○  | ○  | Reset } Reset Button |
| Ground         | ○  | ○  | PWR } Power Button   |
|                | 2  | 1  |                      |

Figure 2-4. JF1 Pin Definitions

## Front Control Panel Pin Definitions

### NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table below for pin definitions.

| NMI Button<br>Pin Definitions (JF1) |            |
|-------------------------------------|------------|
| Pin#                                | Definition |
| 19                                  | Control    |
| 20                                  | Ground     |

### Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table below for pin definitions.

| Power LED<br>Pin Definitions (JF1) |            |
|------------------------------------|------------|
| Pin#                               | Definition |
| 15                                 | +5V        |
| 16                                 | Ground     |

### HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a hard drive LED cable here to display disk activity detected on the motherboard's built-in disk controllers. See the table below for pin definitions.

| HDD LED<br>Pin Definitions (JF1) |            |
|----------------------------------|------------|
| Pin#                             | Definition |
| 13                               | +5V        |
| 14                               | HDD Active |

**NIC1/NIC2 Link LED**

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1, and the LED connection for LAN Port 2 is on Pins 9 and 10. Attach the NIC LED cables to display network activity. Refer to the table below for pin definitions.

| NIC1/NIC2 LED<br>Pin Definitions (JF1) |            |
|--|------------|
| Pin#                                   | Definition |
| 9/11                                   | Vcc        |
| 10/12                                  | Ground     |

**UID LED**

The UID LED is on pins 7 and 8 of JF1. Connect the front panel UID LED to this header to indicate when the rear UID switch is turned on. Refer to the table below for pin definitions.

| UID LED<br>Pin Definitions (JF1) |            |
|----------------------------------|------------|
| Pin#                             | Definition |
| 7                                | Vcc        |
| 8                                | Ground     |

**Power Fail LED**

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table below for pin definitions.

| Power Fail LED<br>Pin Definitions (JF1) |            |
|---|------------|
| Pin#                                    | Definition |
| 5                                       | Vcc        |
| 6                                       | Ground     |

## Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach the hardware reset switch from the computer case to this header to reset the system. Refer to the table below for pin definitions.

| Reset Button<br>Pin Definitions (JF1) |            |
|---------------------------------------|------------|
| Pin#                                  | Definition |
| 3                                     | Reset      |
| 4                                     | Ground     |

## Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power in the suspend mode, press the button for at least 4 seconds. Refer to the table below for pin definitions.

| Power Button<br>Pin Definitions (JF1) |             |
|---------------------------------------|-------------|
| Pin#                                  | Definition  |
| 1                                     | Signal      |
| 2                                     | +3V Standby |

## 2.7 Connectors

### Onboard Fan Headers (FAN1~FAN6)

There are six fan headers on the motherboard. These are 4-pin fan headers; pins 1-3 are backward compatible with traditional 3-pin fans. The onboard fan speeds are controlled by Thermal Management (via Hardware Monitoring) in the BMC. When using Thermal Management setting, please use all 4-pin fans.

| Fan Header<br>Pin Definitions |                     |
|-------------------------------|---------------------|
| Pin#                          | Definition          |
| 1                             | Ground (Black)      |
| 2                             | +12V (Red)          |
| 3                             | Tachometer (Yellow) |
| 4                             | PWM Control (Blue)  |

### Disk-On-Module Power Connector (JSD1 & JSD2)

The Disk-On-Module (DOM) power connector at JSD1 provides 5V power to a solid-state DOM storage device connected to one of the SATA ports. See the table below for pin definitions.

| DOM Power<br>Pin Definitions |            |
|------------------------------|------------|
| Pin#                         | Definition |
| 1                            | 5V         |
| 2                            | Ground     |
| 3                            | Ground     |

### SATA (SATA0, SATA1)

The H12SSW-iN/NT has two available SATA 3.0 ports (SATA0 and SATA1) on the motherboard. These are standard SATA 3.0 ports.

| SATA Connectors<br>Pin Definitions |          |
|------------------------------------|----------|
| Pin#                               | Signal   |
| 1                                  | Ground   |
| 2                                  | SATA_TXP |
| 3                                  | SATA_TXN |
| 4                                  | Ground   |
| 5                                  | SATA_RXN |
| 6                                  | SATA_RXP |
| 7                                  | Ground   |



### NVMe Ports ( NVMe 0/1)

The H12SSW-iN/NT has two dedicated NVMe ports (two ports on a slim SAS connector) on the motherboard. These ports provide high-speed, low-latency PCI-E 3.0 x4 connections directly from the CPU to NVMe Solid State (SSD) drives. This greatly increases SSD data-throughput performance and significantly reduces PCI-E latency by simplifying driver/software requirements resulting from direct PCI-E interface from the CPU to the NVMe SSD drives.

### SATA/NVMe Hybrid Ports (SATA 0-7/NVMe 2-3; SATA 8-15/NVMe 4-5)

Each SATA/NVMe hybrid port can support up to eight SATA 3.0 ports or 2x NVMe ports (PCI-E x4), for a total of 16 SATA ports or four NVMe ports.

### TPM Header/Port 80 Connector (TPM Port)

The TPM header is used to connect a Trusted Platform Module (TPM), which is available from a third-party vendor. A TPM is a security device that supports encryption and authentication in hard drives. It enables the motherboard to deny access if the TPM associated with the hard drive is not installed in the system.

Please go to the following link for more information on TPM: <http://www.supermicro.com/manuals/other/TPM.pdf>.

| Trusted Platform Module Header<br>Pin Definitions |                    |      |                      |
|---|--------------------|------|----------------------|
| Pin#  | Definition         | Pin# | Definition           |
| 1   | LCLK               | 2    | GND                  |
| 3   | LFRAME#            | 4    | Key                  |
| 5   | LRESET#            | 6    | N/C                  |
| 7   | LAD3               | 8    | LAD2                 |
| 9   | 3.3V               | 10   | LAD1                 |
| 11  | LAD0               | 12   | GND                  |
| 13  | SMB_CLK (optional) | 14   | SMB_DAT (optional)   |
| 15  | P3V3_STBY          | 16   | SERIRQ               |
| 17  | GND                | 18   | LP_CLKRUN (optional) |
| 19  | LPC_PD (optional)  | 20   | LPC_DRQ (optional)   |

### PCI-E M.2 Connector (M.2-C1, M.2-C2)

The PCI-E M.2 connectors are for devices such as memory cards, wireless adapters, etc. These devices must conform to the PCI-E M.2 specifications (formerly known as NGFF). These particular PCI-E M.2 supports M-Key (PCI-E x2) storage cards. M.2-C1 can support a speed of PCI-E x4, when one M.2 device is installed.

### USB Ports (USB 0~3, USB 4, USB 5/6)

There are a total of seven USB ports supported on the motherboard. Four are located on the back panel , USB 0/1, and USB 2/3 (both are USB 3.1 compliant). There are also three ports located on the motherboard, two are on one header, USB 5/6 (3.1) and the remaining one is on a 3.1 "Type A" connector (USB 4).

| Front Panel USB 3.0<br>Pin Definitions |            |      |            |
|--|------------|------|------------|
| Pin#                                   | Definition | Pin# | Definition |
| 1                                      | VBUS       | 19   | Power      |
| 2                                      | Stda_SSRX- | 18   | USB3_RN    |
| 3                                      | Stda_SSRX+ | 17   | USB3_RP    |
| 4                                      | GND        | 16   | GNd        |
| 5                                      | Stda_SSTX- | 15   | USB3_TN    |
| 6                                      | Stda_SSTX+ | 14   | USB3_TP    |
| 7                                      | GND        | 13   | GND        |
| 8                                      | D-         | 12   | USB_N      |
| 9                                      | D+         | 11   | USB_P      |
| 10                                     |            | x    |            |

### Expansion Slots

The motherboard features several expansion slots. The table below describes each slot's type and speed.

| Expansion Slots |  |
|-----------------|--|
| Name            | Description  |
| <b>SXB1A</b>    | Riser Slot   |
| <b>SXB1B</b>    | Riser Slot (for CPU Slot1 PCI-E 4.0 x16, Slot3, PCI-E 4.0 x16) |
| <b>SXB1C</b>    | Riser Slot   |
| <b>SXB2</b>     | Riser Slot (for CPU Slot2, PCI-E 4.0 x16)                      |

### Onboard Battery (BATTERY)

The onboard back up battery is located at BATTERY. The onboard battery provides backup power to the on chip CMOS, which stores the BIOS' setup information. It also provides power to the Real Time Clock (RTC) to keep it running.

### Chassis Intrusion (JL1)

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to the header to inform you when the chassis is opened.

| Chassis Intrusion<br>Pin Definitions |                 |
|--------------------------------------|-----------------|
| Pins                                 | Definition      |
| 1                                    | Ground          |
| 2                                    | Intrusion Input |

### Overheat LED/Fan Fail (JOH1)

The JOH1 header is used to connect an LED indicator to provide warnings of chassis overheating or fan failure. This LED will blink when a fan failure occurs. Refer to the table on right for pin definitions.

| Overheat / Fan Fail<br>Pin Definitions |            |
|--|------------|
| Pins                                   | Definition |
| 1                                      | 5vDC       |
| 2                                      | OH Active  |

| Overheat / Fan Fail<br>LED Status |             |
|-----------------------------------|-------------|
| Status                            | Definition  |
| Off                               | Normal      |
| On, Solid                         | Overheat    |
| On, Blinking                      | Fan Failure |

### IPMB System Management Bus Header (JIPMB1)

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I<sup>2</sup>C connection on your system.

| IPMB Header<br>Pin Definitions |               |
|--------------------------------|---------------|
| Pin#                           | Definition    |
| 1                              | Data          |
| 2                              | Ground        |
| 3                              | Clock         |
| 4                              | No Connection |

### Main Power Supply Connector (JPWR2)

The primary power supply connector (JPWR2) is an ATX power connector that the power supply plugs into directly.

| ATX Power 24-pin Connector<br>Pin Definitions |            |       |            |
|---|------------|-------|------------|
| Pin#  | Definition | Pin # | Definition |
| 13  | +3.3V      | 1     | +3.3V      |
| 14  | -12V       | 2     | +3.3V      |
| 15  | Ground     | 3     | Ground     |
| 16  | PS_ON      | 4     | +5V        |
| 17  | Ground     | 5     | Ground     |
| 18  | Ground     | 6     | +5V        |
| 19  | Ground     | 7     | Ground     |
| 20  | Res (NC)   | 8     | PWR_OK     |
| 21  | +5V        | 9     | 5VSB       |
| 22  | +5V        | 10    | +12V       |
| 23  | +5V        | 11    | +12V       |
| 24  | Ground     | 12    | +3.3V      |

### 12V 8-pin Auxilliary Power Connector (JPWR1)

JPWR1 is an 8-pin ATX power input to provide auxiliary power to the processor. Refer to the table below for pin definitions.

| 12V 8-pin Power Connector<br>Pin Definitions |            |
|--|------------|
| Pins   | Definition |
| 1 through 4                                  | Ground     |
| 5 through 8                                  | +12V       |

### Power SMB Header (PWRI2C)

Power System Management Bus (I2C) header monitors power supply, fan and system temperatures. See the table below for pin definitions.

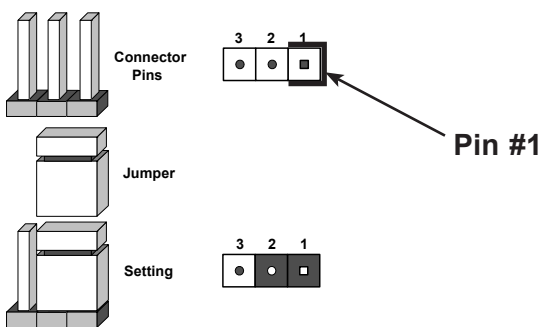
| Power SMB Header<br>Pin Definitions |            |
|-------------------------------------|------------|
| Pin#                                | Definition |
| 1                                   | Clock      |
| 2                                   | Data       |
| 3                                   | PWR Fail   |
| 4                                   | Ground     |
| 5                                   | +3.3V      |

## 2.8 Jumper Settings

### How Jumpers Work

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin #1 is identified with a thicker border line on the printed circuit board. See the diagram below for an example of jumping pins 1 and 2. Refer to the motherboard layout page for jumper locations.

**Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



### Clear CMOS (JBT1)

JBT1 is used to clear CMOS, which will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

#### **To Clear CMOS**

1. First power down the system and unplug the power cord(s).
2. Remove the cover of the chassis to access the motherboard.
3. Remove the onboard battery from the motherboard.
4. Short the CMOS pads with a metal object such as a small screwdriver for at least four seconds.
5. Remove the screwdriver (or shorting device).
6. Replace the cover, reconnect the power cord(s), and power on the system.

**Note:** Clearing CMOS will also clear all passwords.

*Do not use the PW\_ON connector to clear CMOS.*



**Watch Dog (JWD1)**

JWD1 controls the Watch Dog function. Watch Dog is a monitor that can reboot the system when a software application hangs. Jumping pins 1-2 will cause Watch Dog to reset the system if an application hangs. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that hangs. Watch Dog must also be enabled in BIOS. The default setting is Reset.

**Note:** When Watch Dog is enabled, the user needs to write their own application software to disable it.

| Watch Dog<br>Jumper Settings |                 |
|------------------------------|-----------------|
| Jumper Setting               | Definition      |
| Pins 1-2                     | Reset (Default) |
| Pins 2-3                     | NMI             |
| Open                         | Disabled        |

**LAN Enable/Disable (JPL1)**

Jumper JPL1 will enable or disable LAN1/LAN2. See below for jumper settings. The default setting is enabled.

| GLAN Enable<br>Jumper Settings |                   |
|--------------------------------|-------------------|
| Pin#                           | Definition        |
| 1-2                            | Enabled (default) |
| 2-3                            | Disabled          |

## 2.9 LED Indicators

### LAN Port LEDs

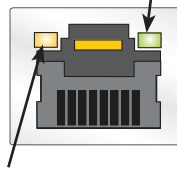
The motherboard's Ethernet ports has two LED indicators. The Activity LED is green and indicates connection and activity. The Link LED may be green, orange/amber, or off to indicate the speed of the connection. Refer to the tables below for more information.

#### H12SSW-NT

| Link LED, Connection Link, Speed Indicator |            |
|--|------------|
| LED Color                                  | Definition |
| Orange                                     | 1 Gb/s     |
| Green                                      | 10 Gb/s    |

| Activity LED |               |            |
|--------------|---------------|------------|
| Color        | State         | Definition |
| None         | No Connection |            |
| None         | Off           | Link       |
| Green        | Flashing      | Active     |

#### Activity LED



#### Link LED

#### H12SSW-iN

| Link LED, Connection Link, Speed Indicator |            |
|--|------------|
| LED Color                                  | Definition |
| Off  | 10 Mb/s    |
| Green                                      | 100 Mb/s   |
| Orange                                     | 1 Gb/s     |

| Activity LED |               |            |
|--------------|---------------|------------|
| Color        | State         | Definition |
| None         | No Connection |            |
| None         | Off           | Link       |
| Green        | Flashing      | Active     |

### UID LED Indicator (LED1)

The rear LED1 is located next to the UID switch. The front UID LED is located on the front panel. When you press the UID switch, both rear LED1 and front UID LED indicators will turn on. Press the UID switch again to turn off the LED indicators. Use this UID Indicator to 'mark' the system, so the system can be easily identified whether on the front or back (e.g., a system rack with multiple units installed). See also UID LED on page 35.

| UID LED LED Indicator |          |                 |
|-----------------------|----------|-----------------|
| Color                 | State    | Definition      |
| Blue                  | Solid On | Unit Identified |
| None                  | Off      | UID Off         |

### BMC Heartbeat LED (LED7)

A BMC Heartbeat LED is located at LED7 on the motherboard. When LED7 is blinking, the BMC is functioning normally. See the table below for more information.

| BMC Heartbeat LED State |               |                   |
|-------------------------|---------------|-------------------|
| Color                   | State         | Definition        |
| Green                   | Solid On      | BMC is not ready  |
| Green                   | Blinking      | BMC Normal        |
| Green                   | Fast Blinking | BMC: Initializing |

**Onboard Power OK LED (LED6)**

LED6 is an onboard power OK LED. When this LED6 is lit, it means the system is turned on, and all the system power rails are ready. When the system is turned off, or any one of the system power rails fail, this LED will turn off. Turn off the system, and unplug the power cable before removing or installing any component(s).

| Onboard Power LED Indicator |  |
|-----------------------------|--|
| LED Color                   | Definition                             |
| Off                         | System Off (power cable not connected) |
| Green                       | System On, Power OK                    |

**M.2 Interface Detection LED (LED11 and LED12)**

The M.2 Interface Detection LED indicates that an M.2 interface was detected and working correctly on either M.2-C1 (LED11) or M.2-C2 (LED12). It will indicate whether a SATA or PCI-E signal has been detected.

| M.2 Active LED Indicator |             |
|--------------------------|-------------|
| LED Color                | Definition  |
| Off                      | Off (PCI-E) |
| Green                    | On (SATA)   |



# Chapter 3

## Troubleshooting

### 3.1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components.

#### **Before Power On**

1. Check that the power LED on the motherboard is on.
2. Make sure that the power connector is connected to your power supply.
3. Make sure that no short circuits exist between the motherboard and chassis.
4. Disconnect all cables from the motherboard, including those for the keyboard and mouse.
5. Remove all add-on cards.
6. Install at least one DIMM, a CPU, and a heatsink (in that order), check all jumper settings. Make sure that the heatsink is fully seated.
7. Use the correct type of onboard CMOS battery (CR2032) as recommended by the manufacturer. To avoid possible explosion, do not install the CMOS battery upside down.

#### **No Power**

1. Make sure that no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

## No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Check if memory module population is supported following guidelines on page 31, and re-seat memory DIMM module(s).

**Note:** If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended.

## System Boot Failure

If the system does not display POST (Power-On-Self-Test) or does not respond after the power is turned on, check the following:

1. Clear the CMOS settings by unplugging the power cord and contacting both pads on the CMOS Clear Jumper (JBT1). Refer to chapter 2.8..
2. Remove all components from the motherboard, especially the DIMM modules. Make sure that system power is on.
3. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

## Memory Errors

1. Make sure that the DIMM modules are properly and fully installed.
2. Confirm that you are using the correct memory. Also, it is recommended that you use the same memory type and speed for all DIMMs in the system. See Section 2.4 for memory details.
3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.
4. Check the power supply voltage 115V/230V switch.

## When the System Keeps Losing its Setup Configuration

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1 for details on power supplies.

2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the setup configuration problem, contact your vendor for repairs.

## **When the System Becomes Unstable**

### ***A. If the system becomes unstable during or after OS installation, check the following:***

1. CPU/BIOS support: Make sure that your CPU is supported and that you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.

**Note:** Refer to the product page on our website at <http://www.supermicro.com> for memory and CPU support and updates.

3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check the system cooling to make sure that all heatsink fans and CPU/system fans, etc., work properly. Check the hardware monitoring settings in the IPMI to make sure that the CPU and system temperatures are within the normal range. Also check the front panel Overheat LED and make sure that it is not on.
5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.
6. Proper software support: Make sure that the correct drivers are used.

### ***B. If the system becomes unstable before or during OS installation, check the following:***

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD and CD/DVD-ROM.
2. Cable connection: Check to make sure that all cables are connected and working properly.
3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.

4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

## 3.2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, we do not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please review the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our website before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website. **Note:** Not all BIOS can be flashed depending on the modifications to the boot block code.
3. If you still cannot resolve the problem, include the following information when contacting us for technical support:
  - Motherboard model and PCB revision number
  - BIOS release date/version (this can be seen on the initial display when your system first boots up)
  - System configuration

An example of a Technical Support form is posted on our website.

Distributors: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail.

### 3.3 Frequently Asked Questions

**Question: What type of memory does my motherboard support?**

**Answer:** The H12SSW-iN/NT motherboard supports up to two terabytes of ECC DDR4 2933/3200 MHz speed, RDIMM/LRDIMM/3DS/NVDIMM memory in eight slots. See Section 2.4 for details on installing memory.

**Question: How do I update my BIOS?**

**Answer:** It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <http://www.supermicro.com>.

To update your BIOS:

1. Check the BIOS warning message and the information on how to update your BIOS from our website.
2. Select your motherboard model and check the current BIOS revision to make sure it is newer than your motherboard's installed BIOS before downloading.
3. Download the zip file and save the BIOS package to your computer.
4. Unzip the BIOS files onto a USB stick with a FAT/FAT32 file system.
5. Boot to the motherboard's built-in UEFI Shell and type the following to start the BIOS update process:

```
FLASH.nsh BIOSname#.### <ENTER>
```

**Note:** Supermicro no longer supports the BIOS update method in DOS.

6. Perform an A/C power cycle after the message indicating the BIOS update has completed. You may refer to the "Readme" file in BIOS package for more details.

**Question: Why can't I turn off the power using the momentary power on/off switch?**

**Answer:** The instant power off function is controlled in BIOS by the Power Button Mode setting. When the On/Off feature is enabled, the motherboard will have instant off capabilities as long as the BIOS has control of the system. When the Standby or Suspend feature is enabled or when the BIOS is not in control such as during memory count (the first screen that appears when the system is turned on), the momentary on/off switch must be held for more than four seconds to shut down the system. This feature is required to implement the ACPI features on the motherboard.

**Question:** When I run setup.exe to install AMD SP3 I/O drivers under windows, the Installshield wizard shows “Unable to save file: C:\AMD\AMD\_Chipset\_Drivers\..... The system cannot find the path specified”.

**Answer:** This happens under specific environments. Please click “ok”, and Installshield will prompt you to save the drivers to a folder you designate. It will then run the driver installation automatically.

### 3.4 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (<http://www.supermicro.com/support/rma/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.  
3.5 Battery Removal and Installation

## Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

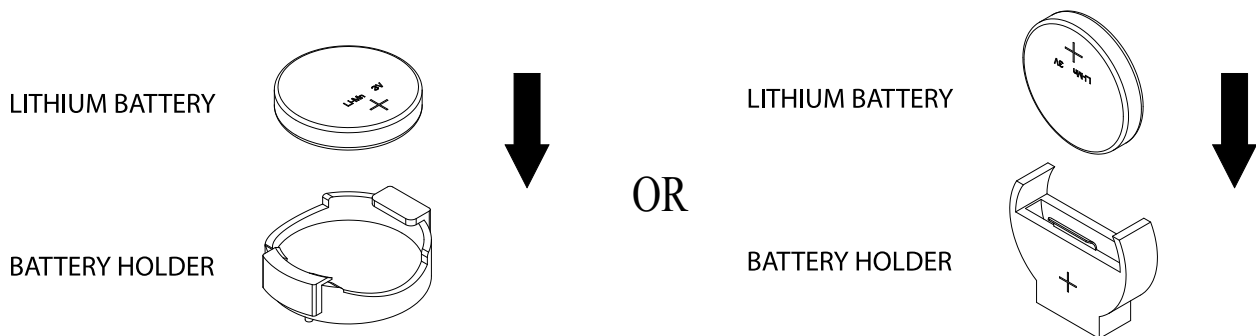
## Proper Battery Disposal

Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

## Battery Installation

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

**Important:** When replacing a battery, be sure to only replace it with the same type.





# Chapter 4

## UEFI BIOS

### 4.1 Introduction

This chapter describes the AMIBIOS™ Setup utility for the H12SSW-iN/NT motherboard. The BIOS is stored on a chip and can be easily upgraded using a flash program.

**Note:** Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

#### Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

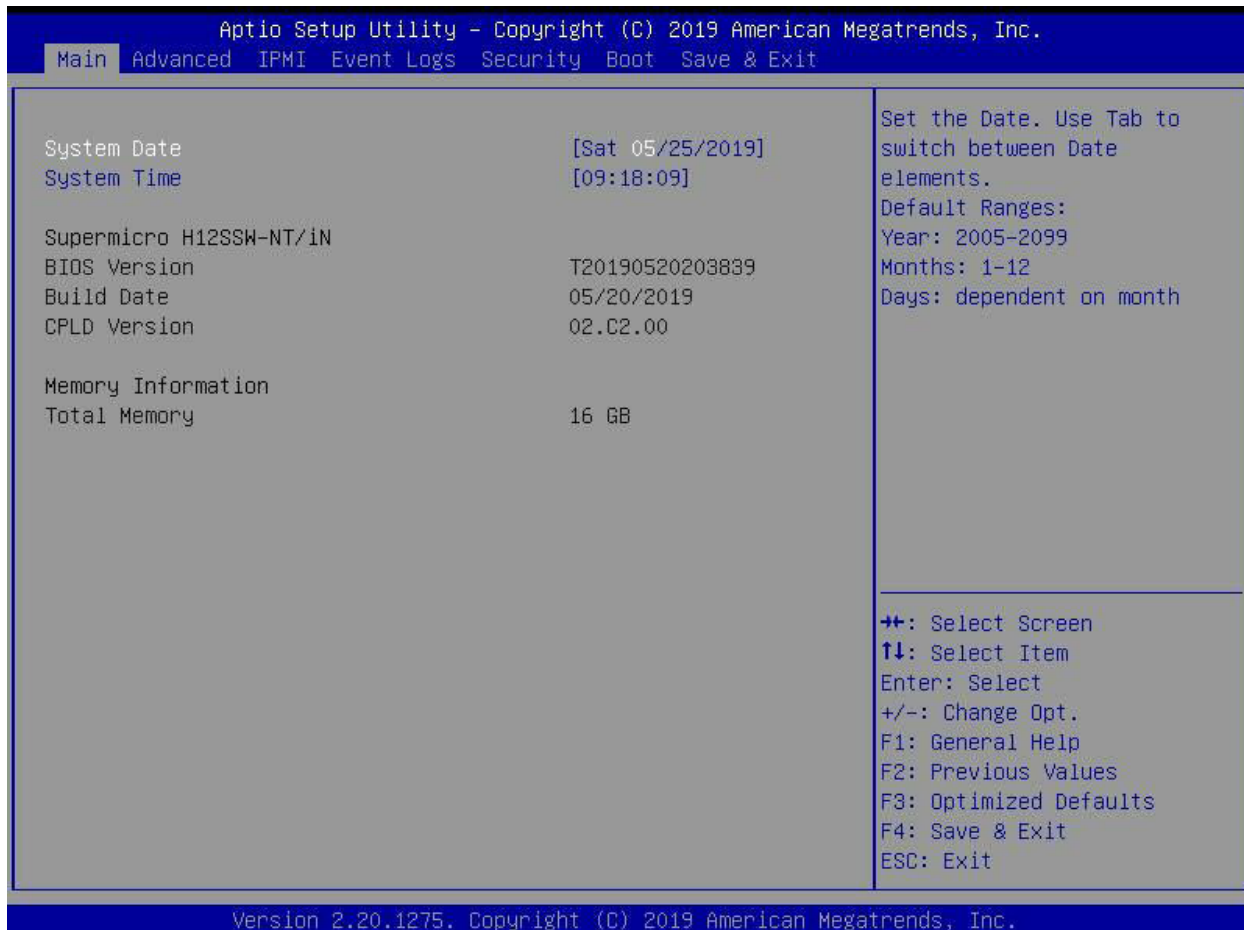
The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A " ►" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F2>, <F3>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

## 4.2 Main Setup

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below. The following Main menu items will be displayed:



### System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in MM/DD/YYYY format. The time is entered in HH:MM:SS format.

**Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is 01/01/2015 after RTC reset.

### Supermicro H12SSW-iN/NT

#### BIOS Version

This item displays the version of the BIOS ROM used in the system.

**Build Date**

This item displays the date when the version of the BIOS ROM used in the system was built.

**CPLD Version**

This item displays the CPLD version of the BIOS ROM used in the system.

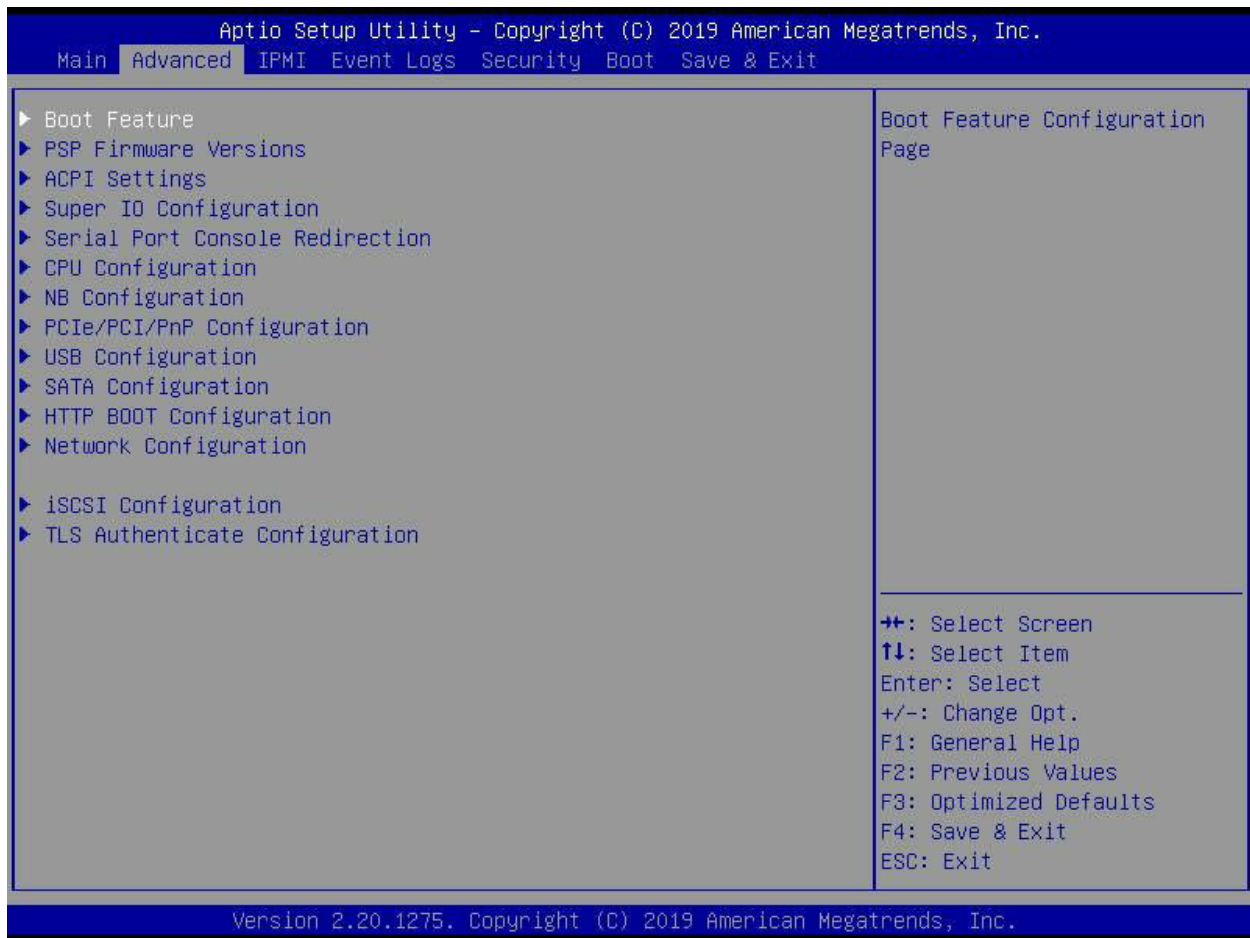
**Memory Information**

**Total Memory**

This feature displays the total system memory detected.

## 4.3 Advanced

Use the arrow keys to select a top item and press <Enter> to access the submenu items.



**Warning:** Take caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect DRAM timing setting may make the system unstable. When this occurs, revert to the default to the manufacture default settings.

### ▶ Boot Feature

#### Quiet Boot

Use this feature to select the screen display between the POST messages and the OEM logo upon bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and Disabled.

#### Option ROM Messages

Use this feature to set the display mode for the Option ROM. Select Keep Current to display the current AddOn ROM setting. Select Force BIOS to use the Option ROM display set by the system BIOS. The options are **Force BIOS** and Keep Current.

**Bootup NumLock State**

Use this feature to set the Power on state for the <Numlock> key. The options are Off and **On**.

**Wait For "F1" If Error**

Use this feature to force the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

**INT19 (Interrupt 19) Trap Response**

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

**Re-try Boot**

If this item is enabled, the BIOS will automatically reboot the system from a specified boot device after its initial boot failure. The options are **Disabled**, Legacy Boot, and EFI Boot.

**Power Configuration****Watch Dog Function**

If enabled, the Watch Dog Timer will allow the system to reset or generate NMI based on jumper settings when it is expired for more than 5 minutes. The options are **Disabled** and Enabled.

**Restore on AC Power Loss**

Use this feature to set the power state after a power outage. Select Stay-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Stay Off, Power On, and **Last State**.

**Power Button Function**

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are **Instant Off** and 4 Seconds Override.

## ► Trusted Computing

### Security Device Support

If this feature and the TPM jumper on the motherboard are both set to Enabled, onboard security devices will be enabled for TPM (Trusted Platform Module) support to enhance data integrity and network security. Please reboot the system for a change on this setting to take effect. The options are Disable and **Enable**.

### TPM State

This software switch will enable or disable the installed security device. The options are Disable and **Enable**.

### Pending Operation

This feature will schedule an operation for the security device, select TPM Clear to clear any pending operation. The options are **None** and TPM Clear

### Device Select

Selecting TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 to TPM 2.0 devices and Auto will auto-select according to the version of the device installed, with the default set to TPM 2.0. The options are TPM 1.2, TPM 2.0 and **Auto**.

## ► PSP Firmware Versions

This section displays the Platform Security Processor (PSP) firmware versions.

### PSP Directory Level 1 (Fixed)

- PSP Recovery BL Ver
- SMU FW Version
- ABL Version
- APCB Version
- APOB Version
- APPB Version

### PSP Directory Level 2 (Updateable)

- PSP Bootloader Version
- SMU FW Version
- ABL Version
- APCB Version
- APOB Version
- APPB Version

## ► ACPI Settings

### High Precision Event Timer

The High Precision Event Timer (HPET) can produce periodic interrupts and is used to synchronize multimedia streams, providing smooth playback and reducing the need to use other timestamp calculations. The options are **Enabled** and Disabled.

### NUMA Nodes Per Socket

This feature specifies the number of desired Non-Uniform Memory Access (NUMA) nodes per socket. Setting this to zero will attempt to interleave the two sockets together. The options are NPS0, NPS1, NPS2, NPS4 and **Auto**.

## ► Super IO Configuration

The following Super IO information will display:

- Super IO Chip AST2500

### ► Serial Port 1 Configuration

#### Serial Port

Select Enabled to enable the selected onboard serial port. The options are Disabled and **Enabled**.

#### Device Settings

This item displays the status of a serial port specified by the user.

#### Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of a serial port specified by the user. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address. The options are **Auto**, (IO=3F8h; IRQ=4;); (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;).

### ► SOL Configuration

#### Serial Port

Select Enabled to enable the selected onboard serial port. The options are Disabled and **Enabled**.

## Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of a serial port specified by the user. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address. The options are **Auto**, (IO=2F8h; IRQ=3;); (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;); and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;).

## ► Serial Port Console Redirection

### COM1

#### Console Redirection

Select Enabled to enable console redirection support for a serial port specified by the user. The options are Enabled and **Disabled**.

*\*If the item above set to Enabled, the following items will become available for user's configuration:*

#### ► Console Redirection Settings

##### Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

##### Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

##### Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and 8.

##### Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark



as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

### **Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

### **Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### **VT-UTF8 Combo Key Support**

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### **Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### **Resolution 100x31**

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### **Putty KeyPad**

This feature selects the settings for Function Keys and KeyPad used for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SC0, ESCN, and VT400.

## **SOL**

### **Console Redirection**

Select Enabled to enable SOL console redirection support for a serial port specified by the user. The options are **Enabled** and Disabled.

***\*If the item above set to Enabled, the following items will become available for user's configuration:***

### **► Console Redirection Settings**

#### **Terminal Type**

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color

and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

### **Bits per second**

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

### **Data Bits**

Use this feature to set the data transmission size for Console Redirection. The options are 7 and 8.

### **Parity**

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

### **Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are 1 and 2.

### **Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### **VT-UTF8 Combo Key Support**

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### **Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### **Resolution 100x31**

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### **Putty KeyPad**

This feature selects the settings for Function Keys and KeyPad used for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SC0, ESCN, and VT400.

## **Legacy Console Redirection**

### **► Legacy Console Redirection Settings**

#### **Redirection COM Port**

For this setting, select a COM port to display redirection of Legacy OS and Legacy OPROM messages. Options include **COM1** and SOL.

#### **Resolution**

For Legacy OS systems, use this setting to specify the number of Rows and Columns supported for redirection. Options include **80x24** and 80x25.

#### **Redirect After Post**

For this setting, when the Bootloader is selected, then the Legacy Console Redirection is disabled before booting to the legacy OS. If you select Always Enable, then the Legacy Console Redirection is enabled for legacy OS systems. Default option for this system is **Always Enable**.

## **Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS)**

### **Console Redirection**

Select Enabled to enable EMS console redirection support for a serial port specified by the user. The options are **Enabled** and Disabled.

***\*If the item above set to Enabled, the following items will become available for user's configuration:***

### **► Console Redirection Settings**

#### **Out-of-Band Mgmt Port**

The feature selects a serial port in a client server to be used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1**, COM2 (Disabled), and AMT SOL.

#### **Terminal Type**

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use

UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, VT100+, **VT-UTF8**, and ANSI.

#### **Bits per Second**

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

#### **Flow Control**

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

#### **Data Bits**

#### **Parity**

#### **Stop Bits**

### **►CPU Configuration**

#### **SMT Control**

Use this setting to specify Simultaneous Multithreading. The options are Disabled and **Auto**.

#### **Core Performance Boost**

This setting is used to configure for Core Performance Boost. Options include **Auto** and Disabled.

#### **Global C-state Control**

This setting is used to configure for Global C-state Control. Options include **Auto**, Disabled and Enabled.

#### **Core Control**

This sets the number of cores to be used by your system. Once this option has been used to remove any cores, a power cycle is required in order for the future selections to take effect. Options include TWO (1+1), Two (2 + 0), Three (3 + 0), Four (2 + 2), Four (4+0), Six (3 + 3) and **Auto**. If unsure, leave this to Auto.

#### **L1 Stream HW Prefetcher / L2 Stream HW Prefetcher**

This setting is used to enable or disable the L1/L2 Stream Hardware Prefetcher. The options are **Auto**, Disabled and Enabled.

## SVM Mode

This setting **Enables** or Disables CPU Virtualization.

### ▶CPU Information

These sections are for informational purposes. They will display some details about the detected CPUs on the motherboard, such as:

- CPU Version
- Number of Cores Running
- Processor Family
- Processor Model
- Microcode Patch Level
- L1 Instruction Cache (Size/Method)
- L1 Data Cache (Size/Method)
- L2 Data Cache (Size/Method)
- L3 Cache per Socket (Size/Method)

### ▶NB Configuration

#### Determinism Slider

Use this setting to configure the Determinism Slider. Options include **Auto**, Power and Performance.

#### cTDP Control

Use this setting to configure the cTDP Control. Options include Manual and **Auto**.

#### IOMMU

Use this setting to enable/disable IOMMU. Options include Enabled, **Disabled** and Auto.

#### ACS Enable

Use this setting to enable/disable ACS. Options include Enabled, Disabled and **Auto**.

### ▶Memory Configuration

#### Memory Clock

This setting allows you to select different memory clock speed. Options include **Auto** and supported frequencies.

### **Memory Interleaving**

This setting controls fabric level memory interleaving. Note that the channel, die and socket have requirements on memory populations and it will be ignored if the memory doesn't support the selected option. The options are Disabled and **Auto**.

### **Memory Interleaving Size**

This setting controls the memory interleaving size. This determines the starting address of the interleave (bit 8, 9, 10 or 11). The options are 256 Bytes, 512 Bytes, 1 KB, 2 KB or **Auto**.

### **Chipset Interleaving**

This setting controls interleave memory blocks across the DRAM chip for node 0. The options are Disabled and **Auto**.

### **BankGroupSwap**

This setting controls the Bank Group Swap. The options are Enabled, Disabled and **Auto**.

## **► CPU1 Memory Information**

These sections are for informational purposes. They will display some details about the detected memory according to each CPU on the motherboard, such as:

- Detected Size (per slot, in MB)
- Current Speed (MT/s)

## **► PCIe/PCI/PnP Configuration**

This menu provides PCIe/PCI/PnP configuration settings and information.

### **PCI Bus Driver Version**

### **Above 4G Decoding**

This setting **Enables** or Disables 64-bit capable devices ability to be decoded in above 4G address space (only if the system supports 64-bit PCI decoding).

### **SR-IOV Support**

If the system has SR-IOV capable PCI-E devices, this setting will Enable or **Disable** the Single Root IO Virtualization Support for the system.

### **BME DMA Mitigation**

Use this setting to re-enable the Bus Master Attribute that was disabled during PCI enumeration for PCI bridges after SMM is locked. The options are Enabled and **Disabled**.

**ARI Forwarding**

If supported by the hardware and set to 'enabled', the downstream port disables its traditional device number field, being 0 enforcement, when turning a type1 configuration request into a type0 configuration request. This permits access to extended functions in an ARI device immediately below the port. The options are Enabled and **Disabled**.

**PCIe Spread Spectrum**

Use this setting to Enable or **Disable** PCI-E Spread Spectrum for your system.

**Target Link Speed**

If supported by hardware and set to Force to x.S GT/S (x being a value of 2.5, 5.0 or 8.0) for Downstream Ports, this sets an upper limit on Link Operational Speed by restricting the values advertised by the UPstream component in its training sequences. When **Auto** is selected, the HW initialized data will be used.

**VGA Priority**

Use this setting to select between onboard or offboard VGA support. The options are **Onboard** and Offboard

**NVMe Firmware Source**

Use this setting to select between the AMI Native firmware support or the device vendor-defined firmware support. The options are AMI Native Support and **Vendor Defined Firmware**.

**M.2 (AHCI) Firmware Source**

Use this setting to select between the AMI Native firmware support or the device vendor-defined firmware support. The options are AMI Native Support and **Vendor Defined Firmware**.

**M.2 HC1 PCI-E 3.0 X4/X2 OPROM****M.2 HC2 PCI-E 3.0 X2 OPROM****RSC-R1UW-2E16 SLOT1 PCI-E X16 OPROM****RSC-R1UW-2E16 SLOT2 PCI-E X16 OPROM****RSC-R1UW-E8R SLOT1 PCI-E X8 OPROM**

This setting enables or disables the listed PCI-E Slot OPROM option. The options are Disabled, **Legacy** or EFI.

**Onboard LAN**

Use this setting to enable or disable the onboard LAN feature. The options are **Enabled** and Disabled..

**Onboard LAN1 Option ROM**

Use this setting to select which option ROM is to be loaded for onboard LAN1 on the system. Options include Disabled, **PXE** and EFI.

### **Onboard LAN2 Option ROM**

Use this setting to select which firmware function is to be loaded for onboard LAN2 on the system. Options include Disabled and **PXE**.

### **Onboard Video Option ROM**

Use this setting to select which firmware function is to be loaded for onboard LAN2 on the system. Options include Disabled, **Legacy** and EFI.

## **► Network Stack Configuration**

### **Network Stack**

This setting allows you to **Enable** or Disable the UEFI Network Stack.

### **Ipv4 PXE Support**

This setting allows you to **Enable** or Disable IPv4 PXE boot support. If disabled, IPv4 PXE boot support will not be available.

### **Ipv4 HTTP Support**

This setting allows you to Enable or **Disable** IPv4 HTTP boot support. If disabled, IPv4 HTTP boot support will not be available.

### **Ipv6 PXE Support**

This setting allows you to **Enable** or Disable IPv6 PXE boot support. If disabled, IPv6 PXE boot support will not be available.

### **Ipv6 HTTP Support**

This setting allows you to Enable or **Disable** IPv6 HTTP boot support. If disabled, IPv6 HTTP boot support will not be available.

### **PXE Boot Wait Time**

This setting allows you to set in a number field the wait time to press the ESC key to abort the PXE boot. Default is **0**.

### **Media Detect Count**

This setting allows you set in a number field the number of times presence of media will be checked. Default is **1**.



## ►USB Configuration

### Legacy USB Support

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disable to have all USB devices available for EFI applications only. The options are **Enabled**, Disabled and Auto.

### XHCI Hand-Off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The options are **Enabled**, and Disabled.

### USB Mass Storage Driver Support

This setting will enable or disable support for USB mass storage drivers. The options are **Enabled**, and Disabled.

### Port 60/64 Emulation

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are **Enabled**, and Disabled.

## ►SATA Configuration

This section displays the detected SATA devices installed on the system.

### ►ASMedia SATA Controller

This setting enables or disables the on chip SATA controller. The options are Enabled, Disabled or **Auto**.

#### Hot Plug

This setting enables or disables the on chip SATA hot plug feature. The options are **Enabled**, Disabled.

#### ASPM Support

This setting enables or disables the ASPM (Active State Power Management) feature. The options are Enabled, **Disabled**.

### ►ASMedia SATA Information

This section displays information on the detected ASMedia SATA devices:

- SATA Mode

## **SATA Configuration**

### **SATA Enable**

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enabled** and Disabled.

### **SATA Hotplug**

This item enables or disables the onboard SATA controller's hot plug feature (PCH). The options are **Enabled** and Disabled.

### **SATA Information**

This item displays information on the detected SATA devices connected and are detected.

## **► HTTP Boot Configuration**

### **HTTP Boot One Time**

When the HTTP boot option is created, this option when enabled, will allow the system to boot via HTTP the first time and revert to the default boot after. The options are Enabled and **Disabled**.

### **Input Description**

This option is an input field that, when the HTTP boot option is created, can be used to enter text to describe or identify the HTTP connection.

### **Boot URI**

This option is an input field used to enter a web or network address to point to the HTTP boot files. This supports the HTTP or HTTPS protocols only.

## ► Network Configuration

(Available when EFI is selected in LAN OPROM after reboot, page 71)

### VLAN Configuration (LAN1 and LAN2)

#### ► Enter Configuration Menu

##### Create New VLAN

##### VLAN ID

This option is an input field used to enter a unique numeric VLAN ID. The valid range is from 0~4096.

##### Priority

This option is an input field used to enter a unique numeric VLAN 802.1Q priority. The valid range is from 0~7.

##### Add VLAN

Click this option to create the new VLAN.

##### Configuration VLAN List

##### Remove VLAN

Click this option to remove an existing VLAN.

### IPv4 Network Configuration

#### Configure

Select Enabled to activate IPv4 network configuration. The options are Enabled and **Disabled**.

***\*If the item above is set to Enabled, the following item will become available for configuration:***

#### Enable DHCP

This feature allows the user to select the source of the IP address for this computer. If Disabled is selected, you will need to know the local IP address of this computer and enter it to the system manually in the field. If Enabled is selected, the system will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that it is attached to and request the next available IP address for this computer. The options are **Enabled** and Disabled.

***\*If the item above is set to Disabled, the following items will become available for configuration:***

### **Local IP Address**

This item sets and displays the Local IP address for this computer. This should be in decimal and in dotted quad form.

### **Local Net Mask**

This item sets the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

### **Local Gateway**

This item sets the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 172.31.0.1).

### **Local DNS**

This item sets the address for the local DNS servers for this computer. This should be in decimal and in dotted quad form (i.e., 172.31.0.1).

### **Save Changes and Exit**

Click this to save the changes above.

## **IPV6 Network Configuration**

This section displays configuration features for IPV6 support.

### **IPV6 Support**

Use this feature to enable IPV6 support. The options are **Enabled** and Disabled.

### **Configuration Address Source**

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **Unspecified**, Static, and DHCP.

***\*If the item "Configuration Address Source" above is set to Static, the following items will become available for configuration:***

- Station IPV6 Address
- Prefix Length
- IPV6 Router1 IP Address

## ► iSCSI Configuration

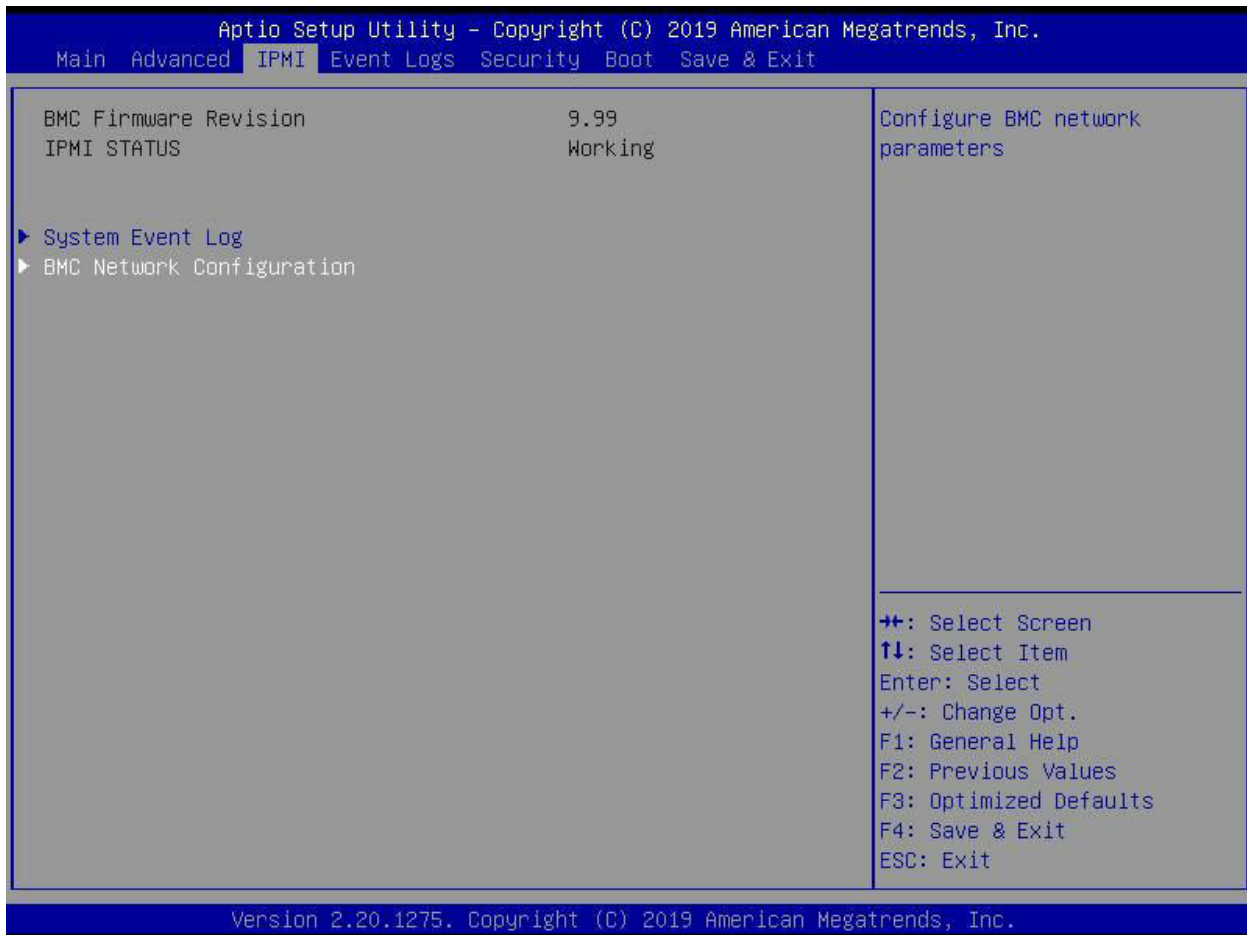
### iSCSI Initiator Name

This feature allows the user to enter the unique name of the iSCSI Initiator in IQN format. Once the name of the iSCSI Initiator is entered into the system, configure the proper settings for the following items.

- Add an Attempt
- Delete Attempts
- Change Attempt Order

## 4.4 IPMI

This tab allows you to configure the following IPMI settings for the system.



Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.

### BMC Firmware Revision

This item indicates the IPMI firmware revision used in your system.

### IPMI Status (Baseboard Management Controller)

This item indicates the status of the IPMI firmware installed in your system.

### ▶ System Event Log

#### Enabling/Disabling Options

##### SEL Components

Select Enabled for all system event logging at bootup. The options are **Enabled** and Disabled.

## Erasing Settings

### Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

### When SEL is Full

This feature allows the user to decide what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.

**Note:** After making changes on a setting, be sure to reboot the system for the changes to take effect.

## ► BMC Network Configuration

### BMC Network Configuration

#### Update IPMI LAN Configuration

Select Yes to enable BMC Network Configuration.

#### IPMI LAN Selection

This item displays the IPMI LAN setting. The default setting is **Failover**.

#### IPMI Network Link Status

This item displays the IPMI Network Link status. The default setting is **Shared LAN**.

#### Update IPMI LAN Configuration

Select Yes for the BIOS to implement all IP/MAC address changes at the next system boot. The options are **No** and Yes.

***\*If the item above is set to Yes, the following item will become available for configuration:***

#### Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **DHCP** and Static.

***\*If the item above is set to Static, the following items will become available for configuration:***

### **Station IP Address**

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form.

### **Subnet Mask**

This item displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

### **Station MAC Address**

This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

### **Gateway IP Address**

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 172.31.0.1).

### **VLAN**

This item displays the virtual LAN settings. The options are Disable and Enable.

## **Configure IPV6 Support**

This section displays configuration features for IPV6 support.

### **IPV6 Support**

Use this feature to enable IPV6 support. The options are **Enabled** and Disabled.

### **Configuration Address Source**

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **Unspecified**, Static, and DHCP.

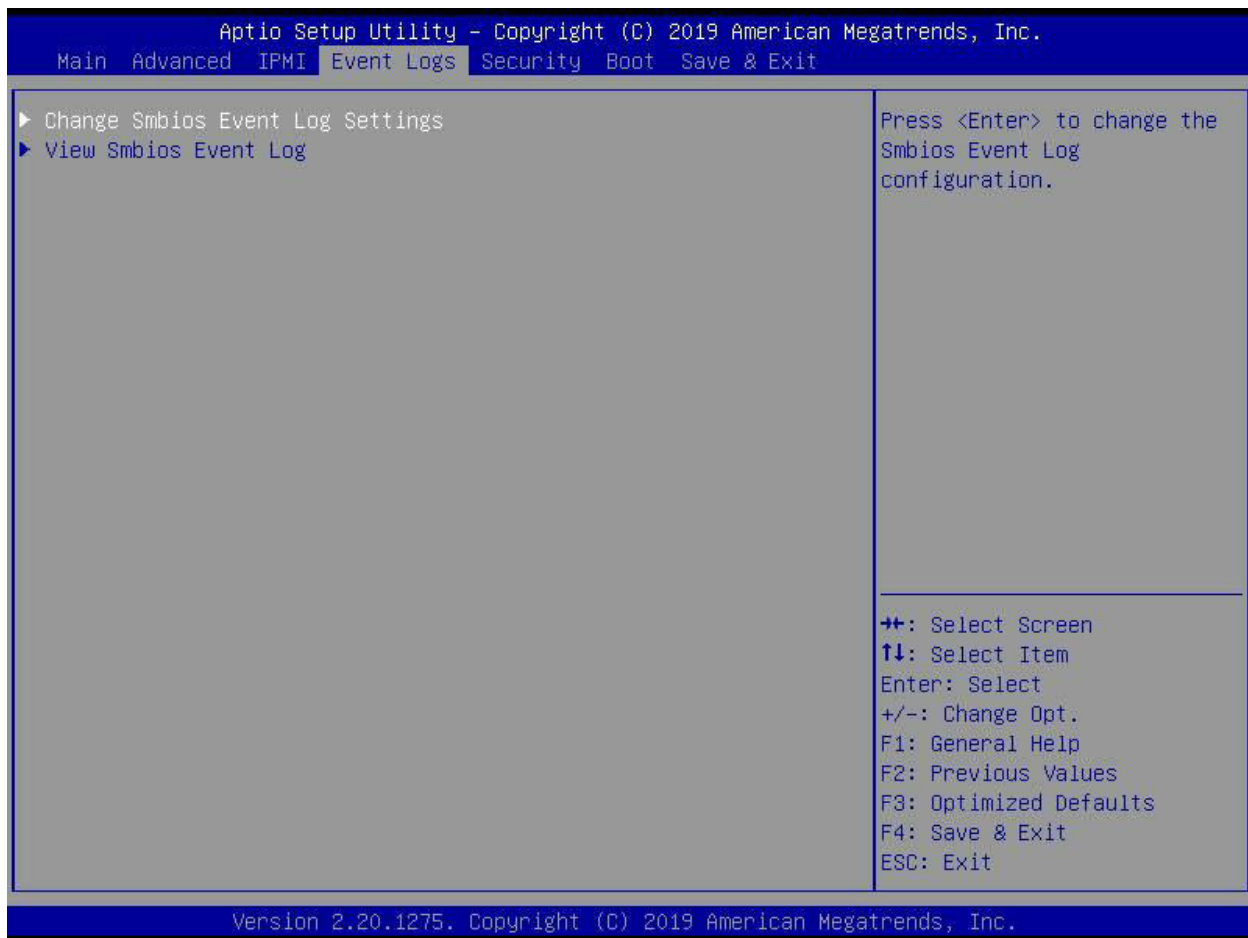
***\*If the item "Configuration Address Source" above is set to Static, the following items will become available for configuration:***

- Station IPV6 Address
- Prefix Length
- IPV6 Router1 IP Address



## 4.5 Event Logs

This tab allows the user to configure the following event logs settings for the system.



### ▶ Change SMBIOS Event Log Settings

This feature allows the user to configure SMBIOS Event settings.

#### Enabling/Disabling Options

##### SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are **Enabled** and Disabled.

#### Erasing Settings

##### Erase Event Log

Select Yes to erase all error events in the SMBIOS (System Management BIOS) log before an event logging is initialized at bootup. The options are **No**, Yes, Next reset, and Yes, every reset.

### **When Log is Full**

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

### **SMBIOS Event Log Standard Settings**

#### **Log System Boot Event**

Select Enabled to log system boot events. The options are **Disabled** and Enabled.

#### **MECI (Multiple Event Count Increment)**

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

#### **METW (Multiple Event Count Time Window)**

This item is used to determine how long (in minutes) the multiple event counter should wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.

**Note:** Please reboot the system for these changes to take effect.

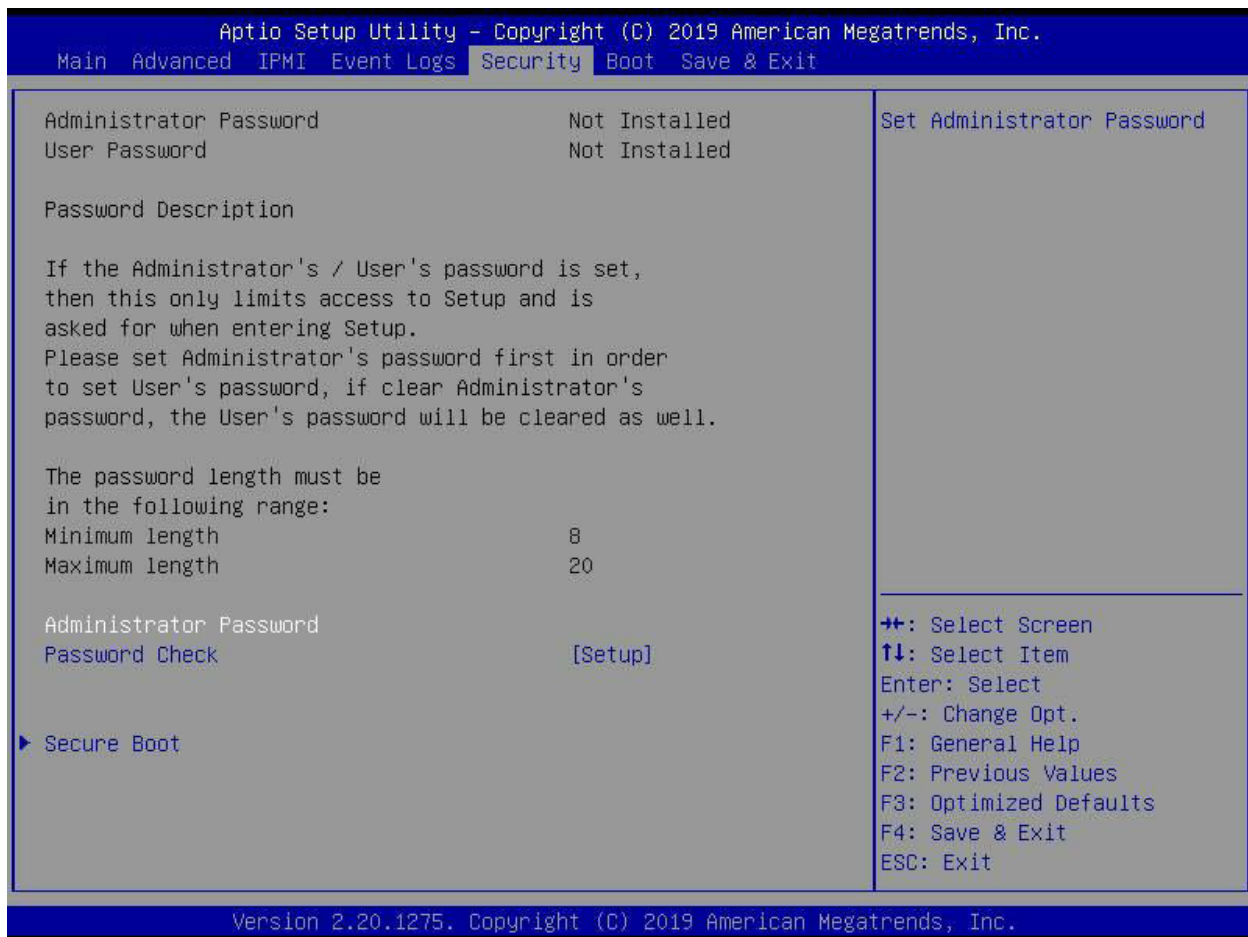
### **►View SMBIOS Event Log**

When Event Logging is on, this item allows the user to view the entries in the SMBIOS event log. The following categories are displayed:

**Date/Time/Error Code/Severity**

## 4.6 Security

This tab allows you to configure the following security settings for the system.



### Administrator Password

Press Enter to create a new, or change an existing Administrator password. Note that if the Administrator Password is erased, the User Password will be cleared as well.

### User Password

Press Enter to create a new, or change an existing User password.

### Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password at bootup or upon entering the BIOS Setup utility. The options are **Setup** and **Always**.

### ► Secure Boot

This section contains options and menus for securing your boot mode and for key management.

### **Secure Boot**

This option allows you specify when the Platform Key (PK) is enrolled. When enabled, the System Mode is user deployed, and the CSM function is disabled. Options include Enabled and **Disabled**.

### **Secure Boot Mode**

Use this item to select the secure boot mode. The options are Standard and **Custom**.

### **CSM Support**

Select Enabled to support the EFI Compatibility Support Module (CSM), which provides compatibility support for traditional legacy BIOS for system boot. The options are Disabled and **Enabled**.

## **▶ Key Management**

This submenu allows the user to configure the following Key Management settings.

### **▶ Install Factory Default Keys**

Select Yes to install all default secure keys set by the manufacturer. The options are **Yes** and No.

### **▶ Enroll EFI Image**

This allows the image to run in Secure Boot Mode, and enroll SHA256 hash of the binary into an Authorized Signature Database (db).

### **▶ Save All Secure Boot Variables**

This feature allows the user to decide if all secure boot variables should be saved.

### **▶ Platform Key (PK)**

This feature allows the user to configure the settings of the platform keys.

### **Set New Key**

Select Yes to load the new platform keys (PK) from the manufacturer's defaults. Select No to load the platform keys from a file. The options are **Yes** and No.

### **Provision Factory Default Keys**

Select Enabled to install the default Secure-Boot keys set by the manufacturer. The options are **Disabled** and Enabled.

### **▶ Key Exchange Keys**

#### **Set New Key**

Select Yes to load the KEK from the manufacturer's defaults. Select No to load the KEK from a file. The options are Yes and No.

**Append Key**

Select Yes to add the KEK from the manufacturer's defaults list to the existing KEK. Select No to load the KEK from a file. The options are Yes and No.

**► Authorized Signatures****Set New Key**

Select Yes to load the database from the manufacturer's defaults. Select No to load the DB from a file. The options are Yes and No.

**Append Key**

Select Yes to add the database from the manufacturer's defaults to the existing DB. Select No to load the DB from a file. The options are Yes and No.

**► Forbidden Signatures****Set New Key**

Select Yes to load the DBX from the manufacturer's defaults. Select No to load the DBX from a file. The options are Yes and No.

**Append Key**

Select Yes to add the DBX from the manufacturer's defaults to the existing DBX. Select No to load the DBX from a file. The options are Yes and No.

**► Authorized TimeStamps****Set New Key**

Select Yes to load the DBT from the manufacturer's defaults. Select No to load the DBT from a file. The options are Yes and No.

**Append Key**

Select Yes to add the DBT from the manufacturer's defaults list to the existing DBT. Select No to load the DBT from a file. The options are Yes and No.

**► OsRecovery Signature**

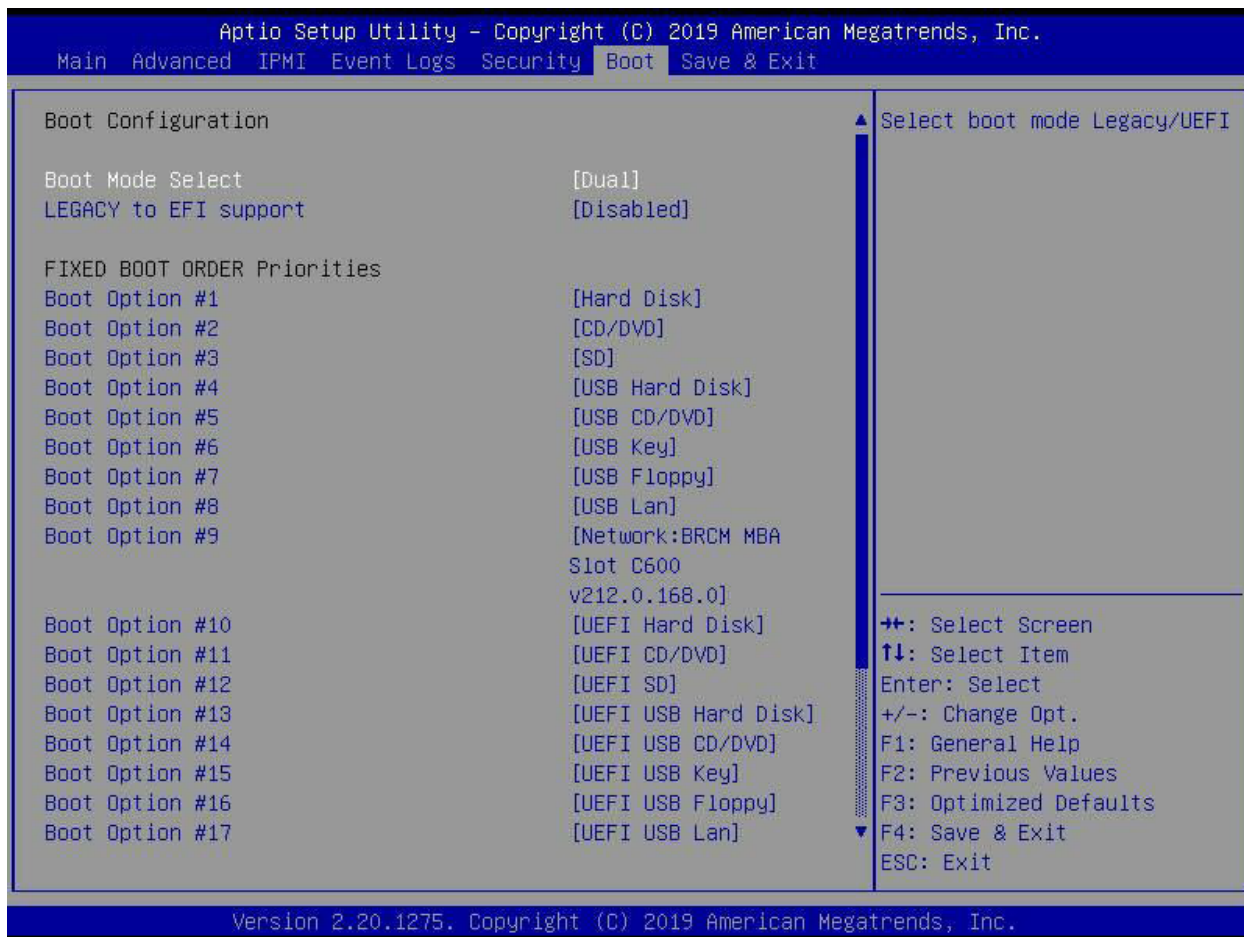
This item uploads and installs an OSRecovery Signature. You may select options for Set New for a factory default key, or select Append to get it from a file. The file formats accepted are:

- 1) Public Key Certificate
  - a. EFI Signature List
  - b. EFI CERT X509 (DER Encoded)
  - c. EFI CERT RSA2048 (bin)
  - d. EFI SERT SHA256 (bin)
- 2) EFI Time Based Authenticated Variable

When prompted, select "Yes" to load Factory Defaults or "No" to load from a file.

## 4.7 Boot

Use this tab to configure Boot Settings:



### Boot Mode Select

Use this item to select the type of device that the system is going to boot from. The options are LEGACY, UEFI, and **DUAL**. The default setting is DUAL.

### Legacy to EFI Support

This option Enables or **Disables** the system to boot to an EFI OS after the boot failed from the legacy boot order.

### FIXED BOOT ORDER Priorities

This option prioritizes the order of bootable devices that the system to boot from. Press <Enter> on each entry from top to bottom to select devices.

### ► Delete Boot Option

Use this feature to remove a pre-defined boot device from which the system will boot during startup. The settings are [any pre-defined boot device].

**▶ UEFI Application Boot Priorities**

This feature allows the user to specify which UEFI devices are boot devices.

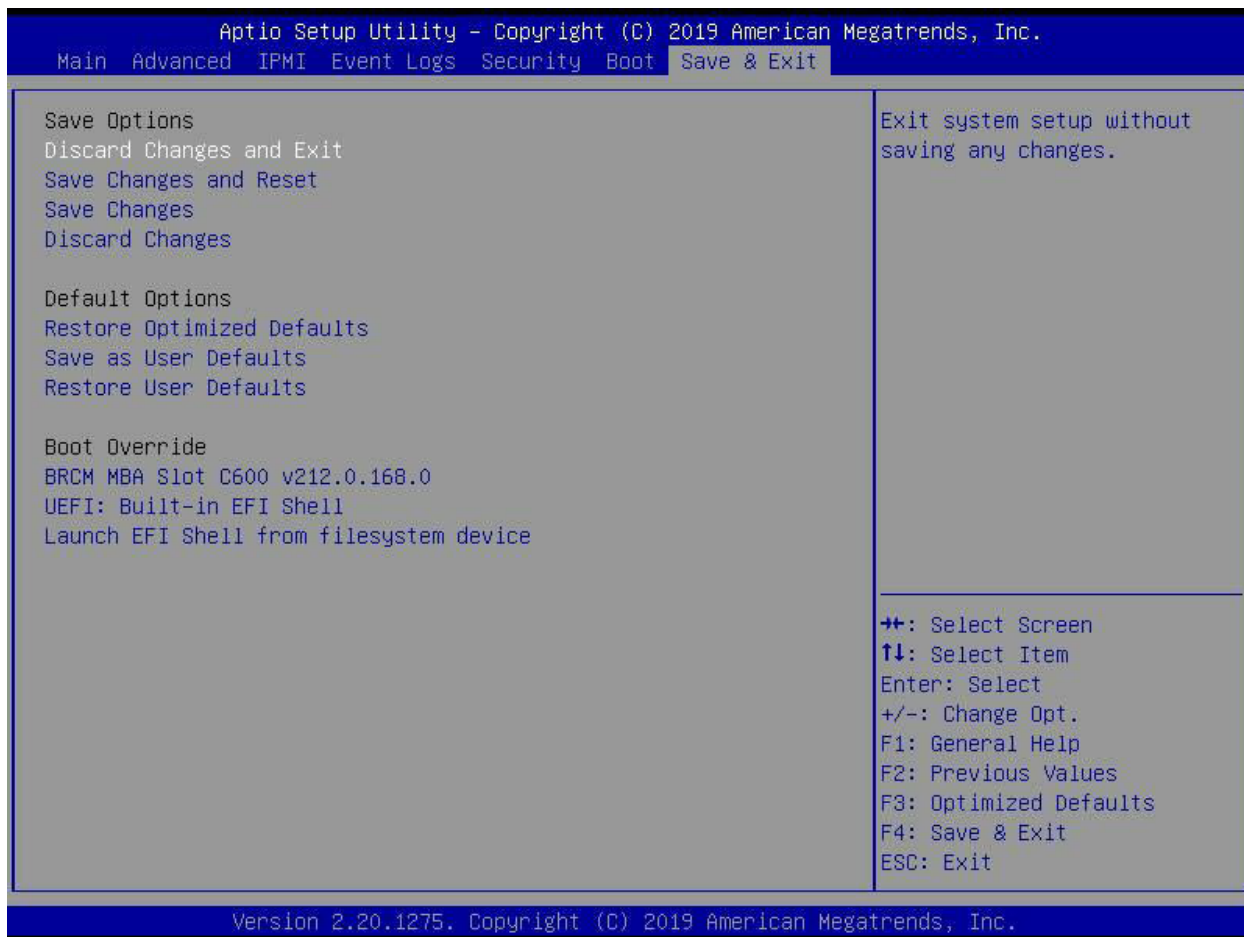
- UEFI Boot Order #1

**▶ NETWORK Drive BBS Priorities**

This feature allows the user to specify which UEFI network drive devices are boot devices.

## 4.8 Save & Exit

Select the Save & Exit tab to enter the Save & Exit BIOS Setup screen.



### Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

### Save Changes and Reset

Select this option to reset the system after saving the changes.

### Save Changes

After completing the system configuration changes, select this option to save the changes you have made. This will not reset (reboot) the system.

### Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS utility Program.



## **Default Options**

### **Restore Defaults**

To set this feature, select Restore Defaults from the Save & Exit menu and press <Enter>. These are factory settings designed for maximum system stability, but not for maximum performance.

### **Save as User Defaults**

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

### **Restore User Defaults**

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

### **Boot Override Section**

Listed on this section are other boot options for the system (i.e., Built-in EFI shell). Select an option and press <Enter>. Your system will boot to the selected boot option.

# Appendix A

## Software Installation

### A.1 Installing Software Programs

The Supermicro FTP site contains drivers and utilities for your system at <ftp://ftp.supermicro.com>. Some of these must be installed, such as the chipset driver.

After accessing the FTP site, go into the CDR\_Images directory and locate the ISO file for your motherboard. Download this file to create a DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

After creating a DVD with the ISO files, insert the disk into the DVD drive on your system and the display shown in Figure B-1 should appear.

Another option is to go to the Supermicro website at <http://www.supermicro.com/products/>. Find the product page for your motherboard here, where you may download individual drivers and utilities to your hard drive or a USB flash drive and install from there.



**Note:** To install the Windows OS, please refer to the instructions posted on our website at <http://www.supermicro.com/support/manuals/>.

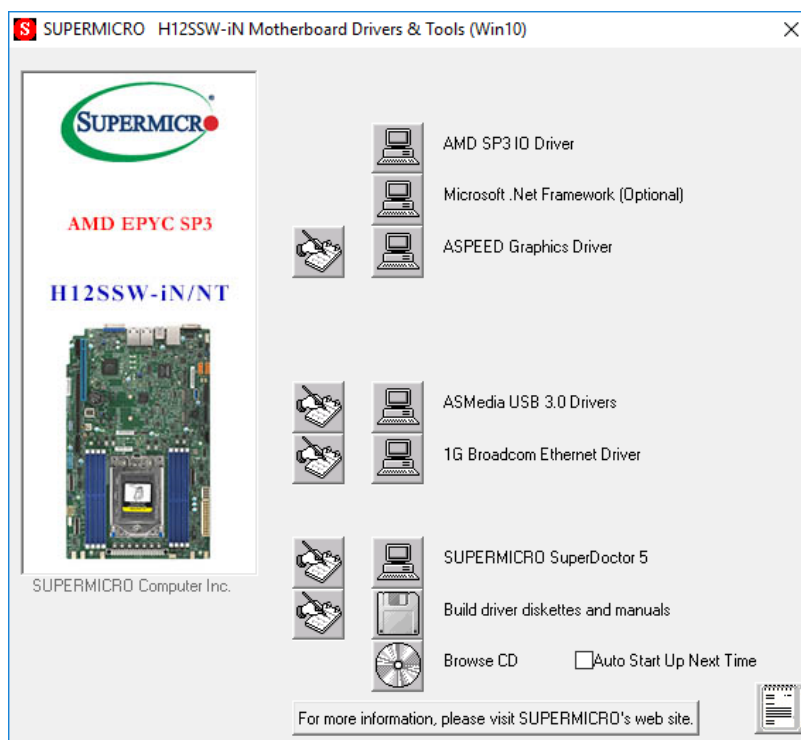


Figure A-1. Driver & Tool Installation Screen

Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to the bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a DVD on it allows you to view the entire contents of the DVD.

When making a storage driver diskette by booting into a driver DVD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

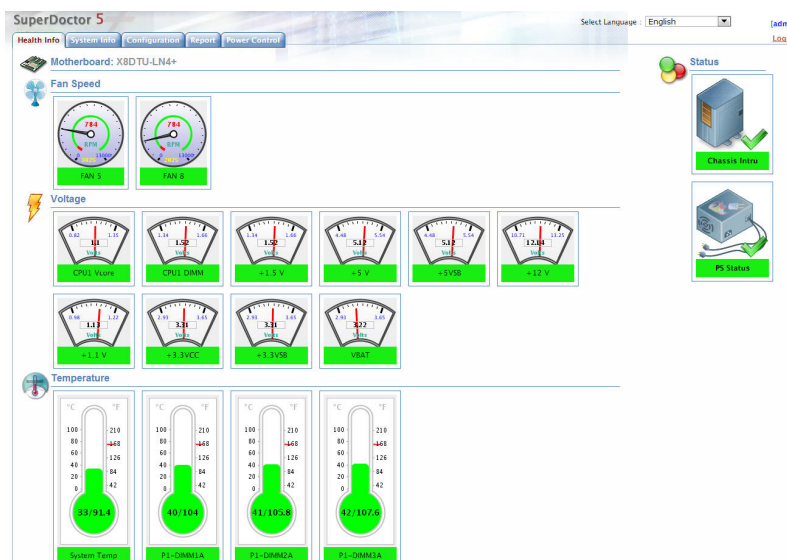
## A.2 SuperDoctor® 5

The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.



**Note:** The default Username and Password for SuperDoctor 5 is ADMIN / ADMIN.



**Figure A-2. SuperDoctor 5 Interface Display Screen (Health Information)**



**Note:** The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at [http://www.supermicro.com/products/nfo/sms\\_sd5.cfm](http://www.supermicro.com/products/nfo/sms_sd5.cfm).

## Appendix B

### Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at [http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm).

#### B.1 Battery Handling



**Warning!** There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

##### 電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

##### 警告

電池更換不當會有爆炸危險。請只使用同類電池或制造商推薦的功能相當的電池更換原有電池。請按製造商的說明處理廢舊電池。

##### 警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

##### Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

## Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

## ¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

**אזהרה!**

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת.

סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة استبدال البطارية بطريقة غير صحيحة فعليك استبدال البطارية فقط بنفس النوع أو ما يعادلها كما أوصت به الشركة المصنعة تخلص من البطاريات المستعملة وفقا لتعليمات الشركة الصانعة

## 경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

## Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

## B.2 Product Disposal



**Warning!** Ultimate disposal of this product should be handled according to all national laws and regulations.

### 製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

### 警告

本产品的废弃处理应根据所有国家的法律和规章进行。

### 警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

### Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

### ¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

### Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

### סילוק המוצר

אזהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

### 경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

### Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

# Appendix C

## UEFI BIOS Recovery

**Warning:** Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating. Doing so may cause a boot failure.

### C.1 Overview

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot the system. The UEFI offers clean, hands-off control to a computer system at bootup.

### C.2 Recovering the UEFI BIOS Image

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is on, the boot block codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.

### C.3 Recovering the BIOS Block with a USB Device

If the BIOS file is corrupted and the system is not able to boot up, this feature will allow you to recover the BIOS image using a USB-attached device. A USB flash drive or a USB CD/DVD ROM drive may be used for this purpose. Please note that a USB hard disk drive is NOT supported. Follow the procedures on the next page to recover the BIOS.

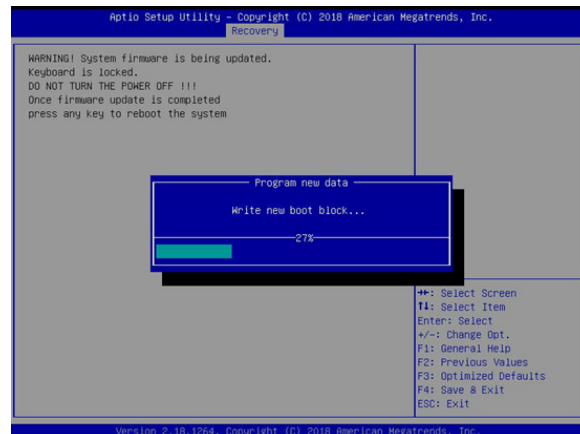
- Using a different system, copy the standard BIOS binary image file into the root directory of a USB flash drive with FAT16 or FAT32 format and rename the file to SUPER.ROM.

**Note:** If you cannot locate the "SUPER.ROM" file in your driver disk, visit our website at [www.supermicro.com](http://www.supermicro.com) to download the correct BIOS image into a USB flash device and rename it "SUPER.ROM".

- While the system is turned off, insert the USB device that contains the new BIOS binary image (SUPER.ROM).
- Power on the system.

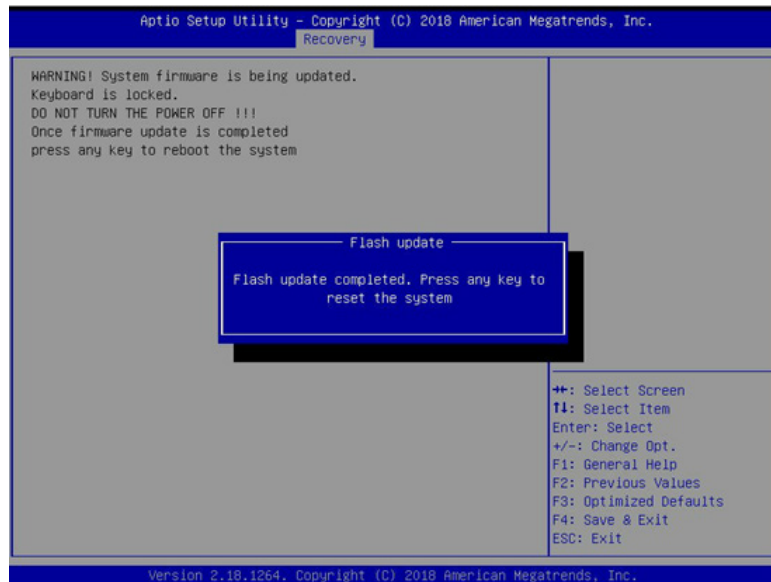


- After the system is turned on, the system will enter the BIOS Recovery menu. Select "Proceed with flash update" to start the BIOS recovery process. **DO NOT INTERRUPT THIS PROCESS UNTIL IT FINISHED!**





5. After the Boot Sector Recovery Process is complete, press any key to reboot the system.

**Notes:**

- \* The BIOS recovery (SUPER.ROM) will not work if BIOS suffered a multi-block crash.
- \* If you have purchased an OOB license, you can do a BIOS flash again through BMC WebGUI or SUM. Please refer to these SUPERMICRO software-related videos:  
Activate OOB license: [https://www.youtube.com/watch?v=V4spX\\_DOV7I](https://www.youtube.com/watch?v=V4spX_DOV7I)  
SUPERMICO BIOS Update: <https://www.youtube.com/watch?v=S8z6iOEHGwY>
- \* If the BIOS flash recovery fails, contact our RMA Department to have the BIOS chip reprogrammed. This will require shipping the board to Supermicro for repair.  
Submit your RMA request at <https://www.supermicro.com/support/rma>  
Please make sure to follow all instructions when returning the motherboard.